

| ZONE | DATE | APPROVE |
|------|----------|---------|
| A | 10/20/68 | WALSH |
| B | 11/20/68 | WALSH |

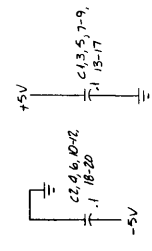
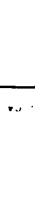
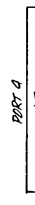
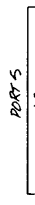
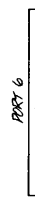
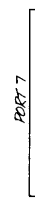
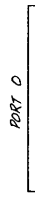
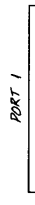
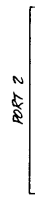
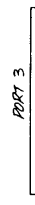
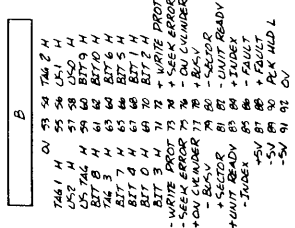
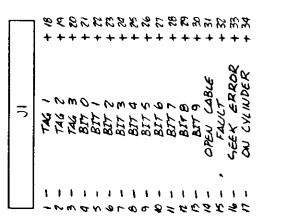
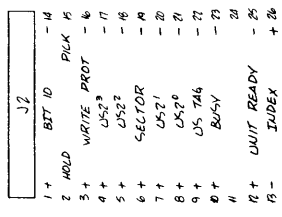
REVISIONS
 DESCRIPTION
 DATE APPROVE

1. ELO FORS & NITAL RELEASE
 2. ECONOMIC INCORPORATED

| LAST DESIGNATION | USED |
|--------------------|------|
| CAPACITOR | C70 |
| CONNECTOR | J10 |
| INTEGRATED CIRCUIT | U17 |
| RESISTOR | R10 |
| RESISTOR MODULE | RM26 |
| TRANSISTOR | Q1 |

| REFERENCE | QTY | USED | PART NUMBER |
|-----------|-----|------|-------------|
| U16 | 5 | 1 | 7425-1A |

NOTES: UNLESS OTHERWISE SPECIFIED
 1. CAPACITANCE VALUES ARE IN MICROFARADS.
 2. RESISTANCE VALUES ARE IN OHMS, 100Ω, 1KΩ, 10KΩ.



| | |
|---------|---|
| CHEET 5 | READ DATA SYNCHRONIZATION AND PORT SELECTOR |
| CHEET 4 | 'A' CABLE INTERFACE |
| CHEET 3 | 'B' CABLE INTERFACE |
| CHEET 2 | 'X' CABLE INTERFACE |

| | |
|-------------|---------|
| DESIGNED BY | G. RATT |
| CHECKED | |
| ENGINEER | |
| APPROVED | |
| DATE | 10-22 |

| | |
|--------------|------|
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |

| | |
|--------------|-------|
| DATE | 10-22 |
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |

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|--------------|-------|
| DATE | 10-22 |
| SCALE | 1:1 |
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| SCALE | 1:1 |
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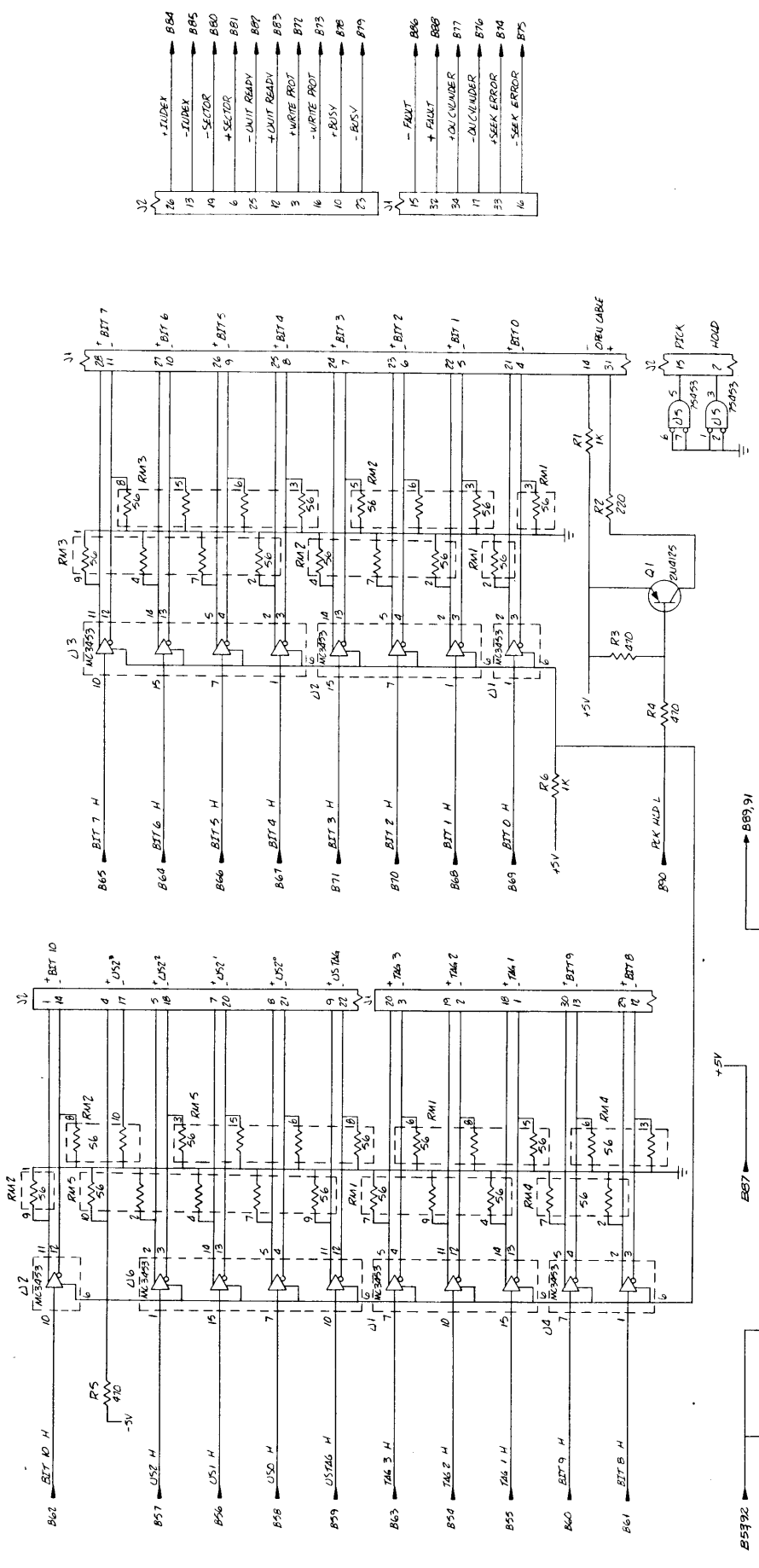
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|--------------|-------|
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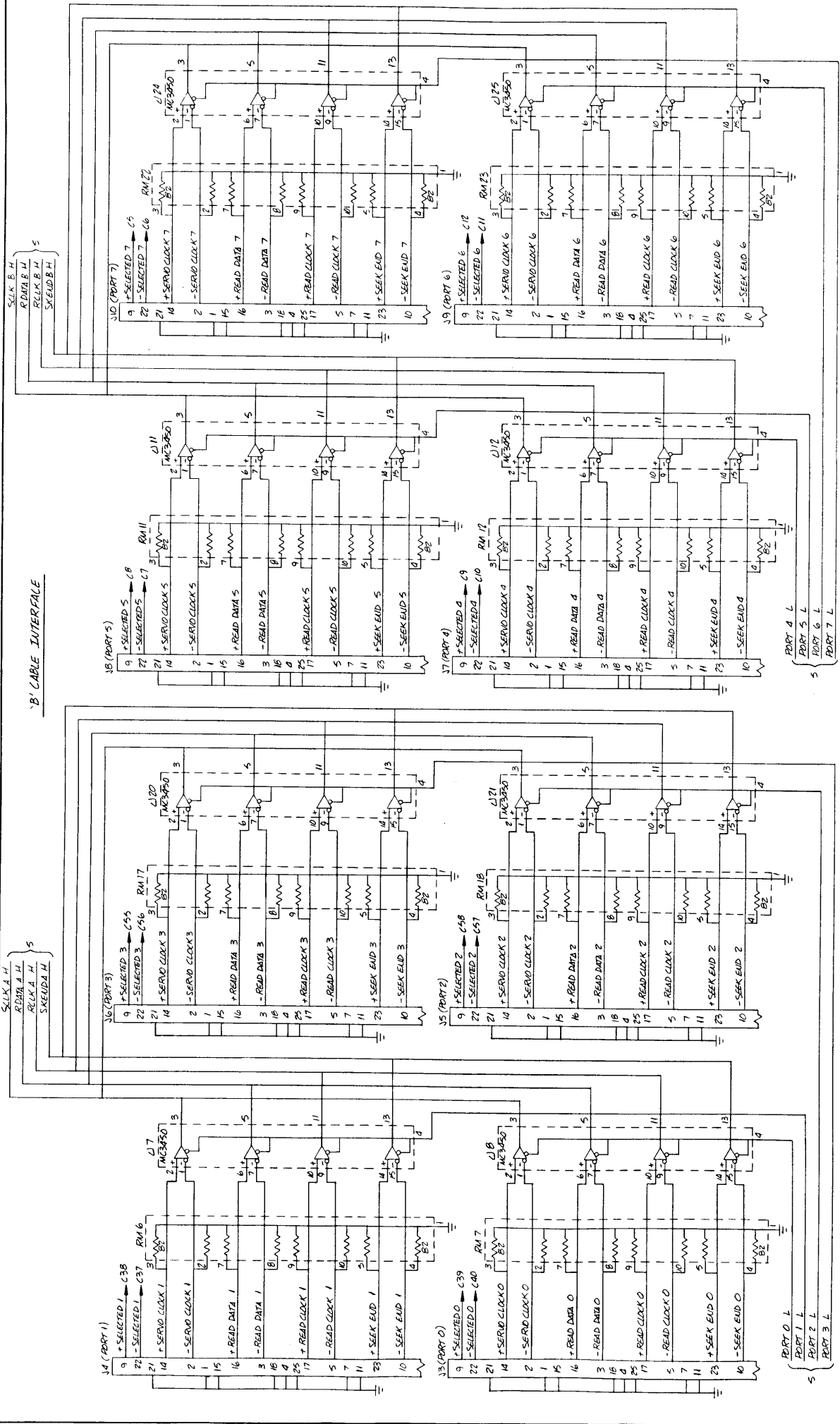
| | |
|--------------|-------|
| DATE | 10-22 |
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |
| SCALE | 1:1 |
| SCALE NUMBER | 1000 |

1' CABLE INTERFACE

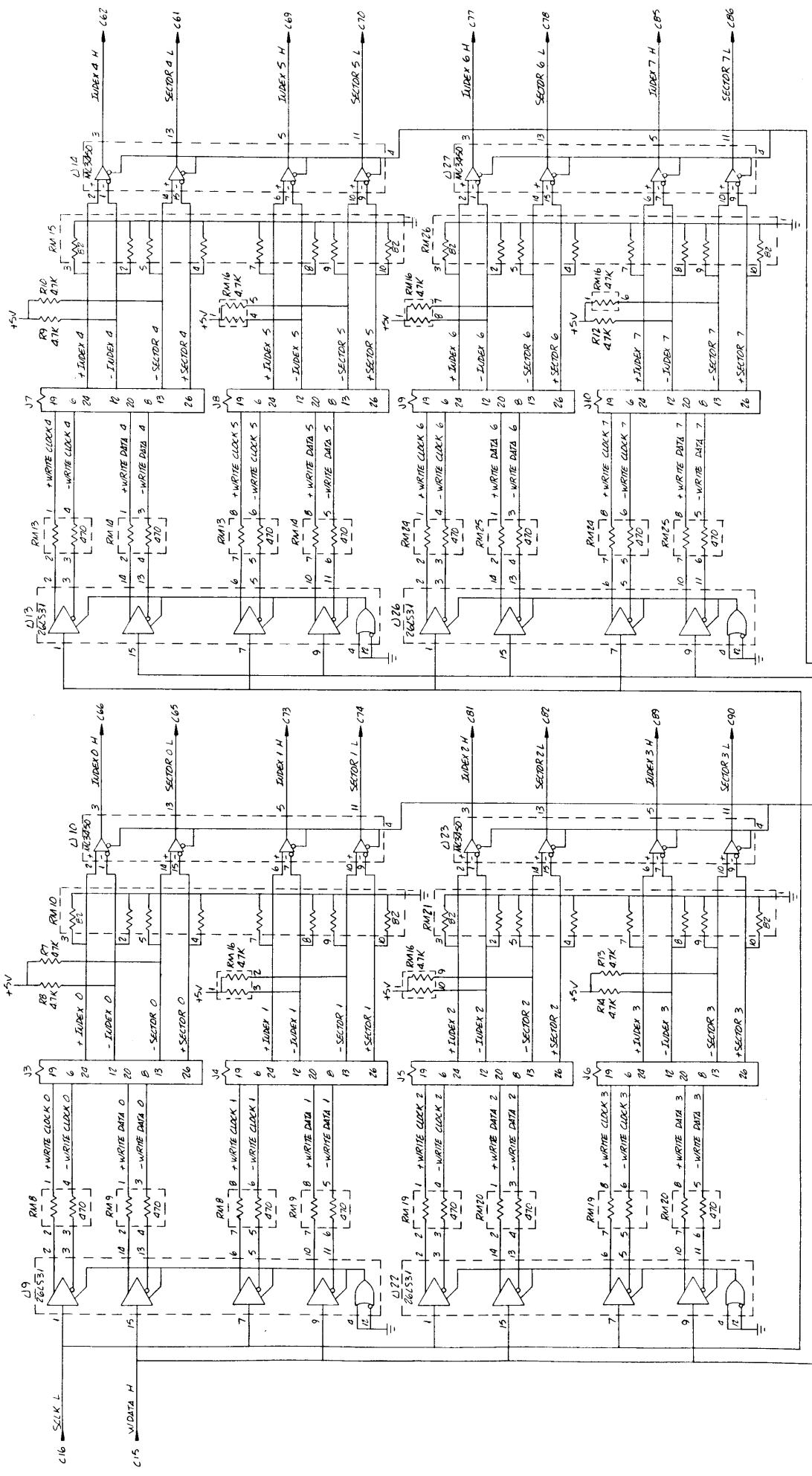


CS 4, 13, 14, 17, 18, 21,
22, 25, 26, 29, 30, 33,
34, 41, 42, 53, 54, 57,
60, 63, 64, 67, 68, 71,
72, 75, 76, 79, 80, 83,
84, 87, 88, 92

8' CABLE INTERFACE



'B' CABLE INTERFACE



5 REV I/11 H

