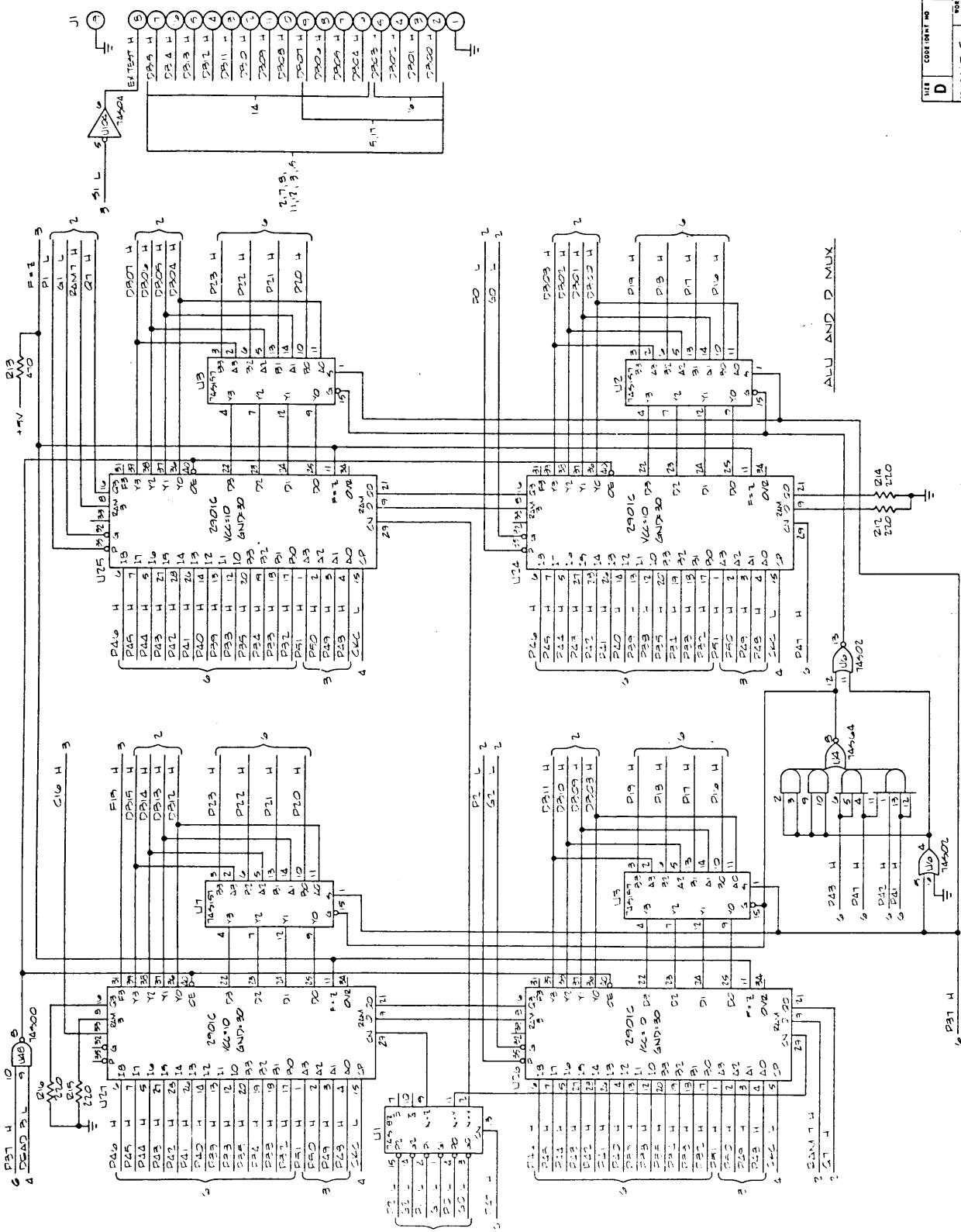
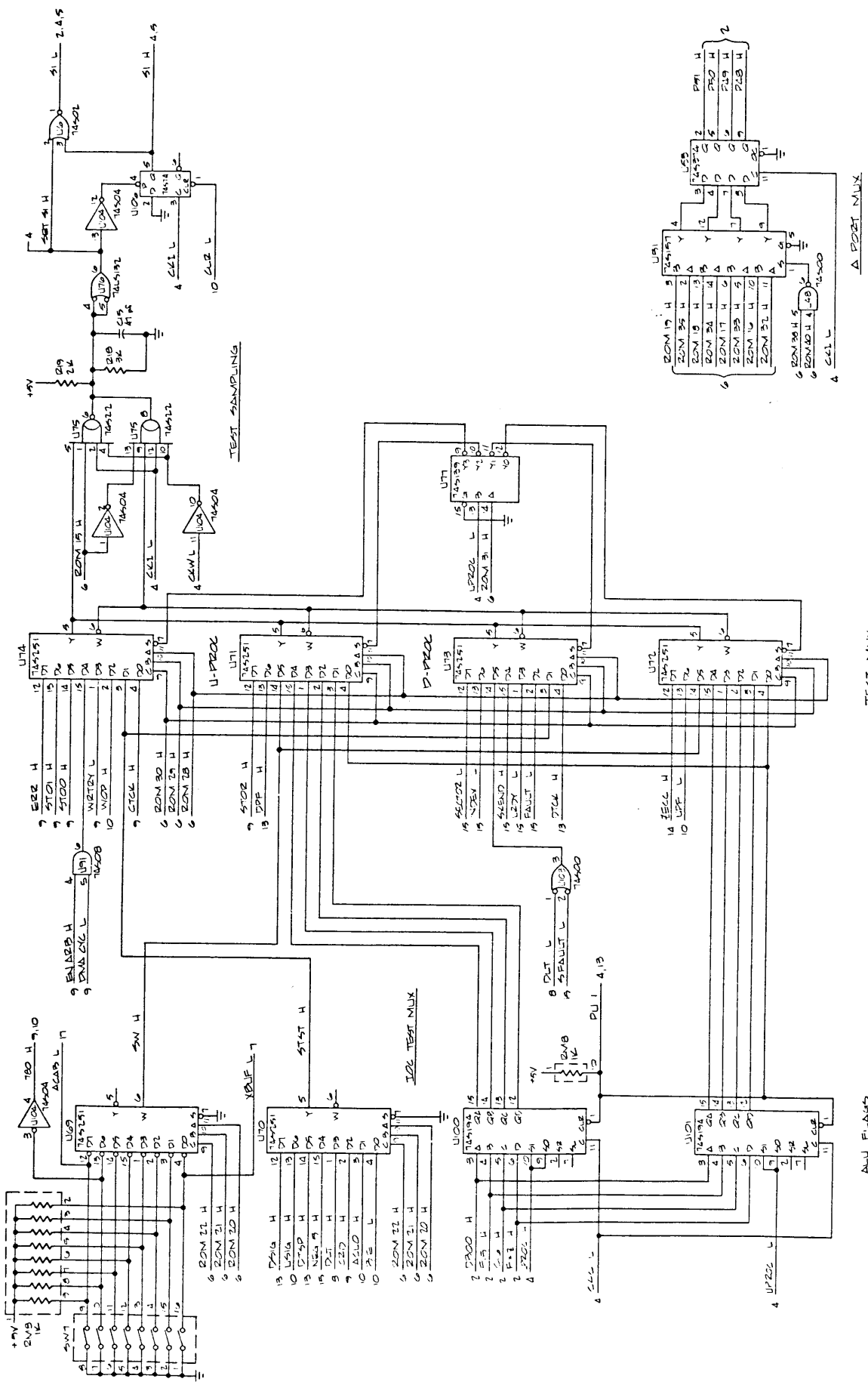


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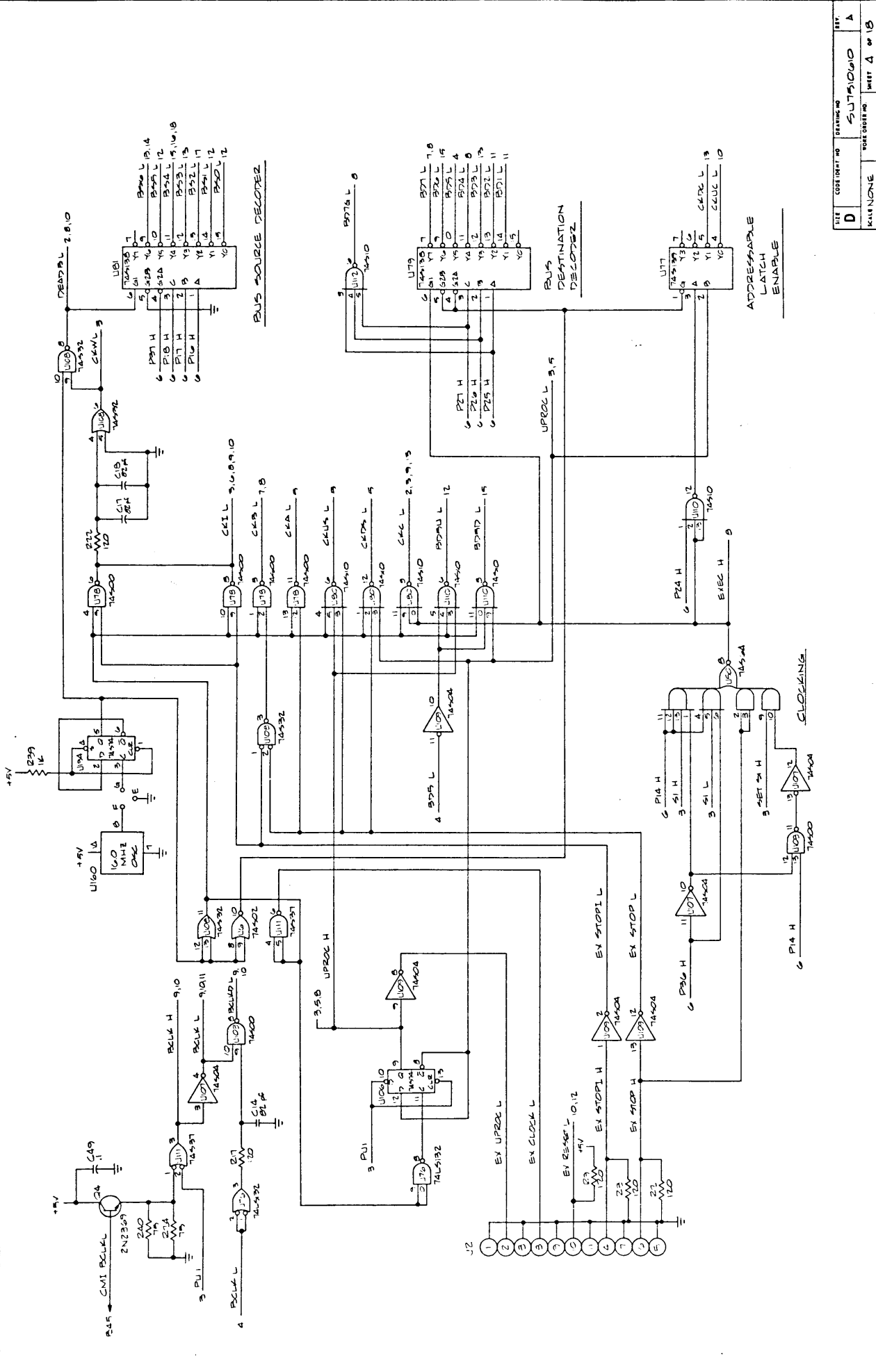


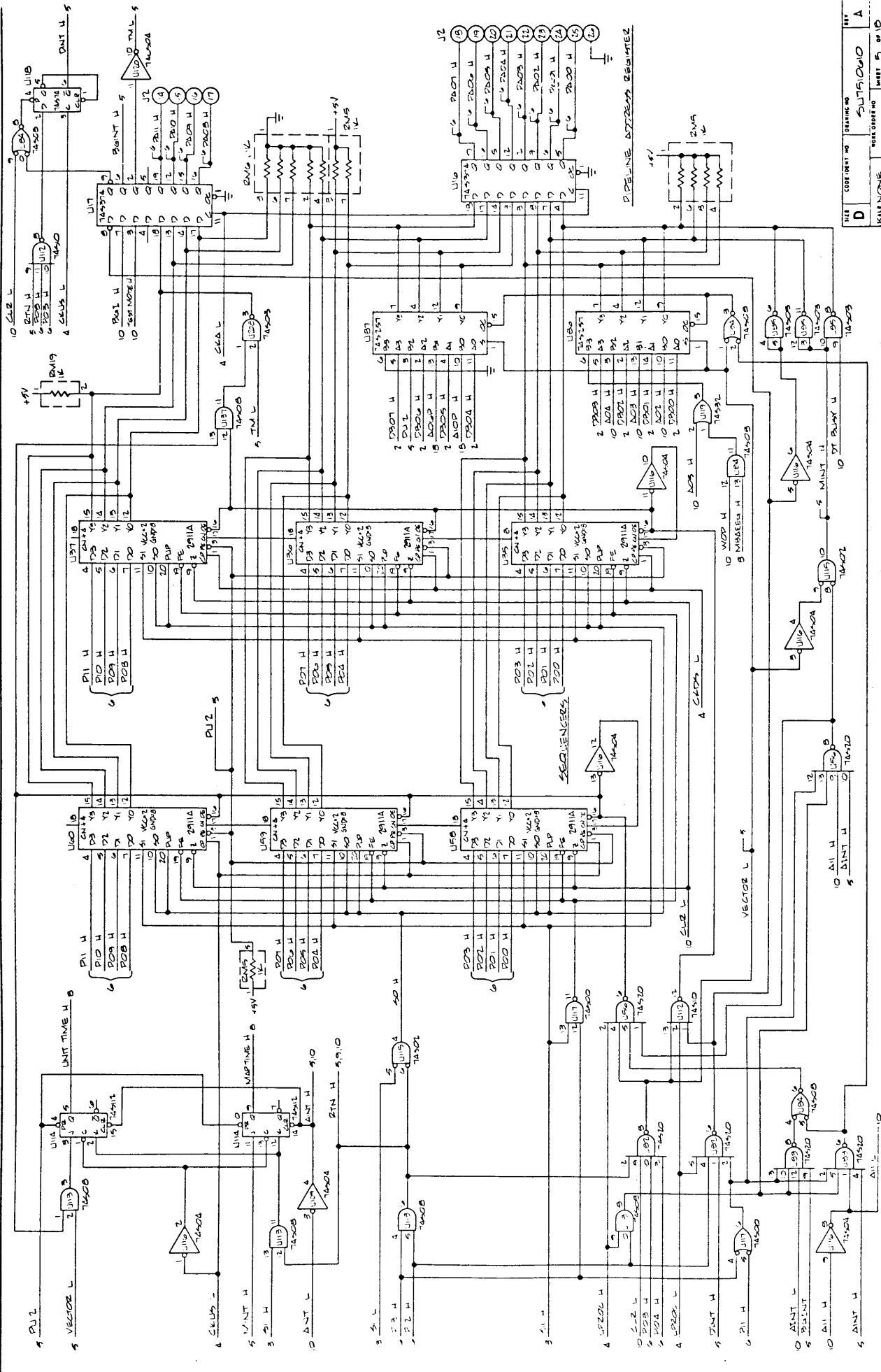


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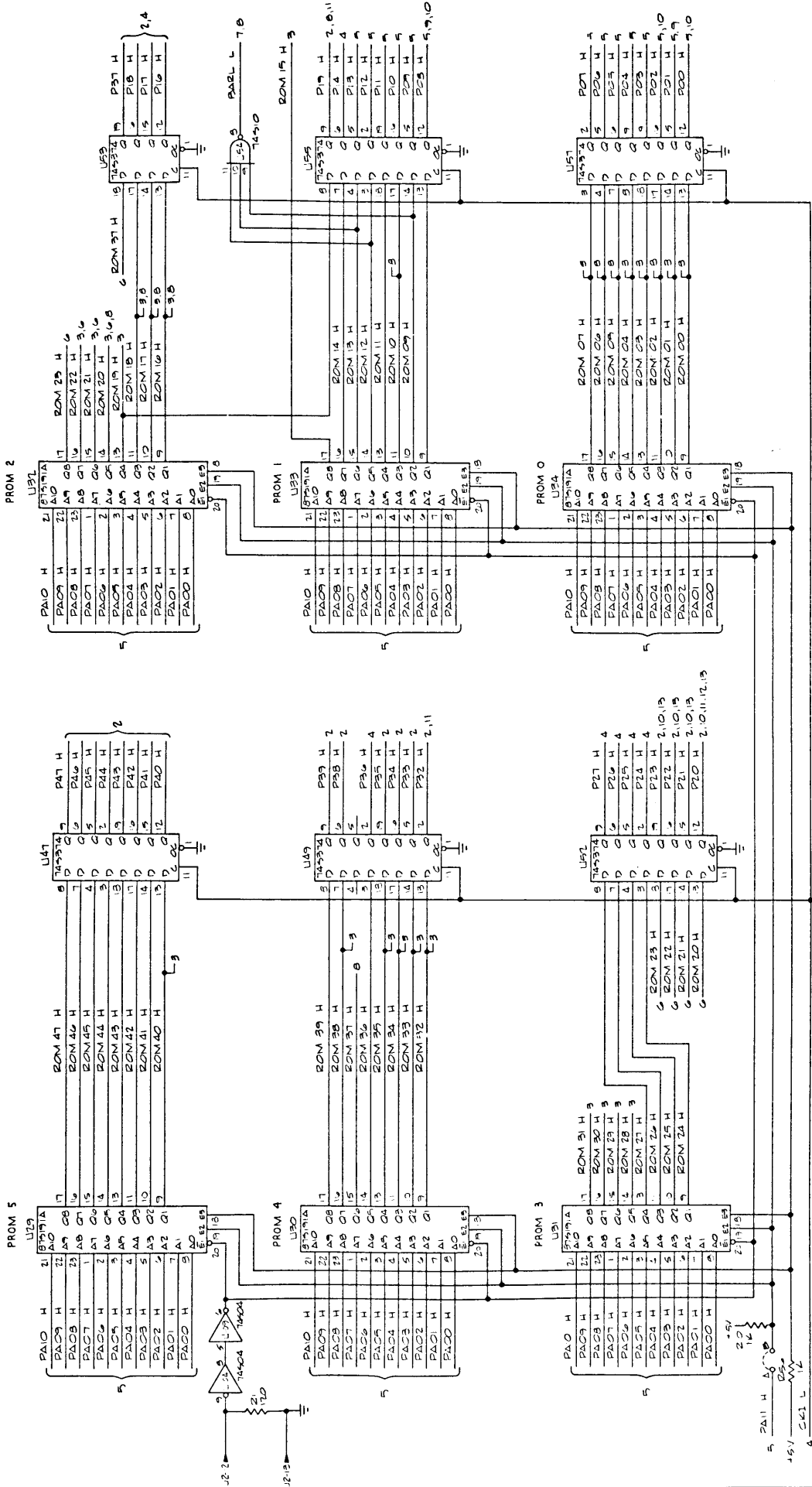
ALU FLA55

A POST MUX



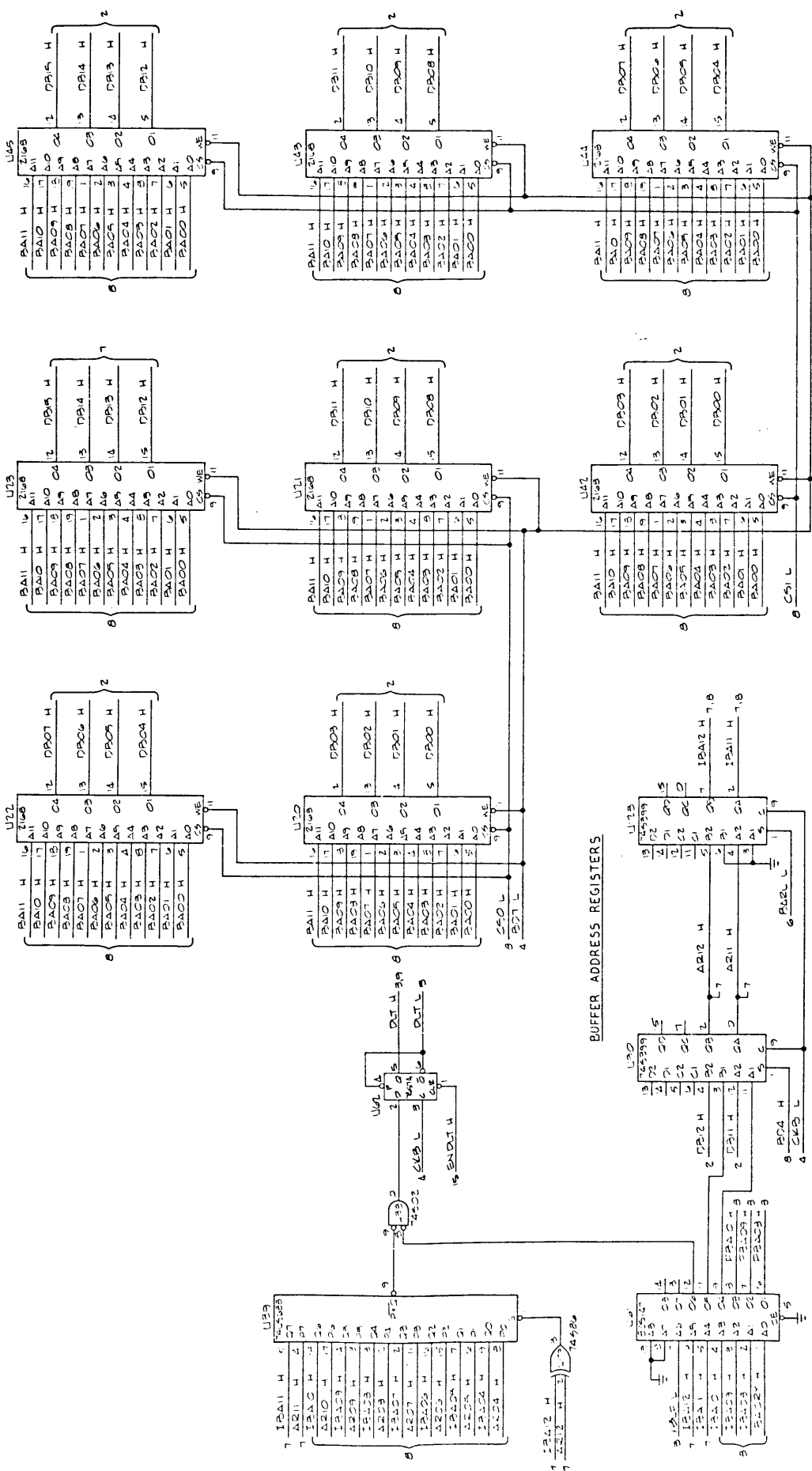


PROMS AND PIPELINE REGISTER

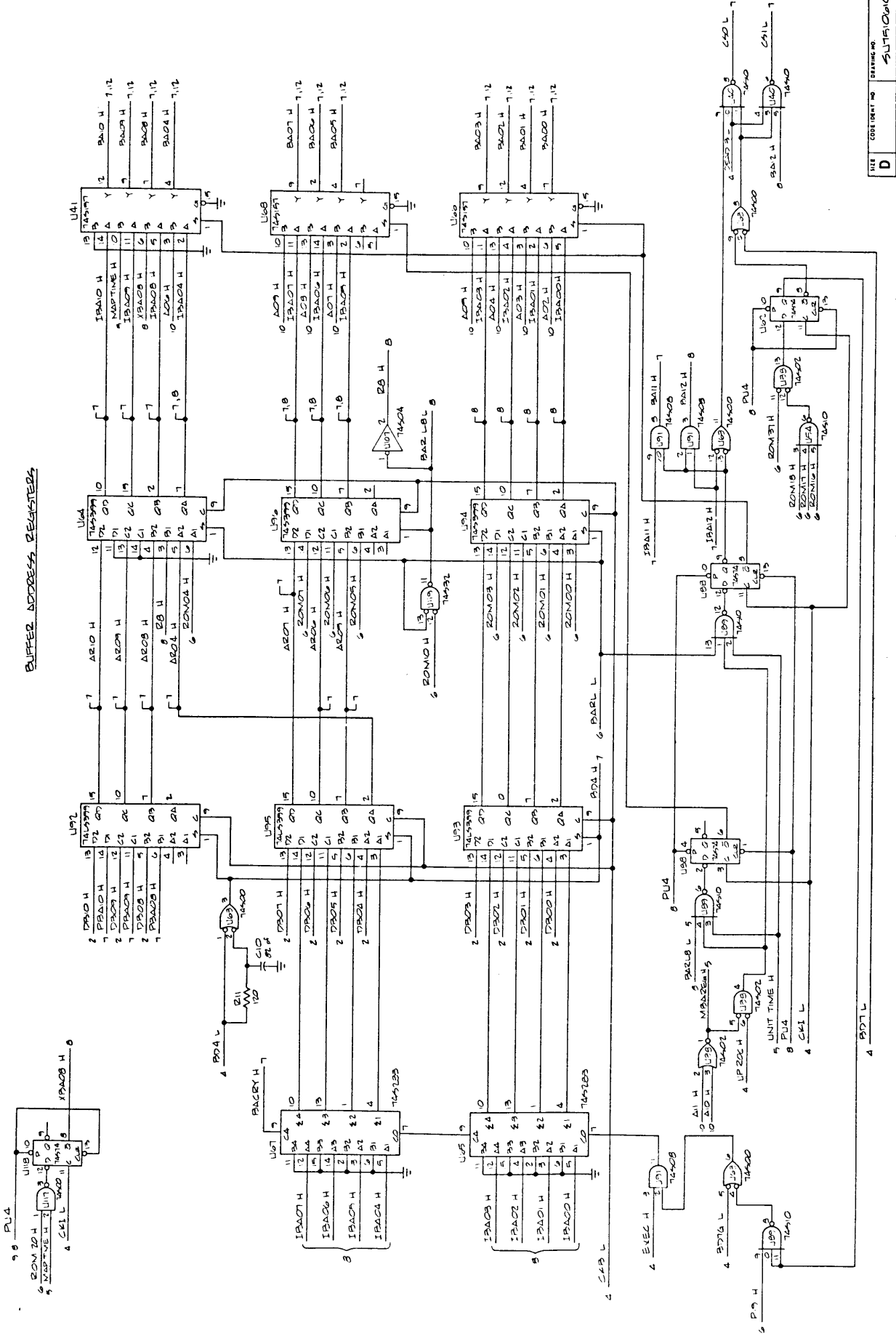


BUFFER MEMORY

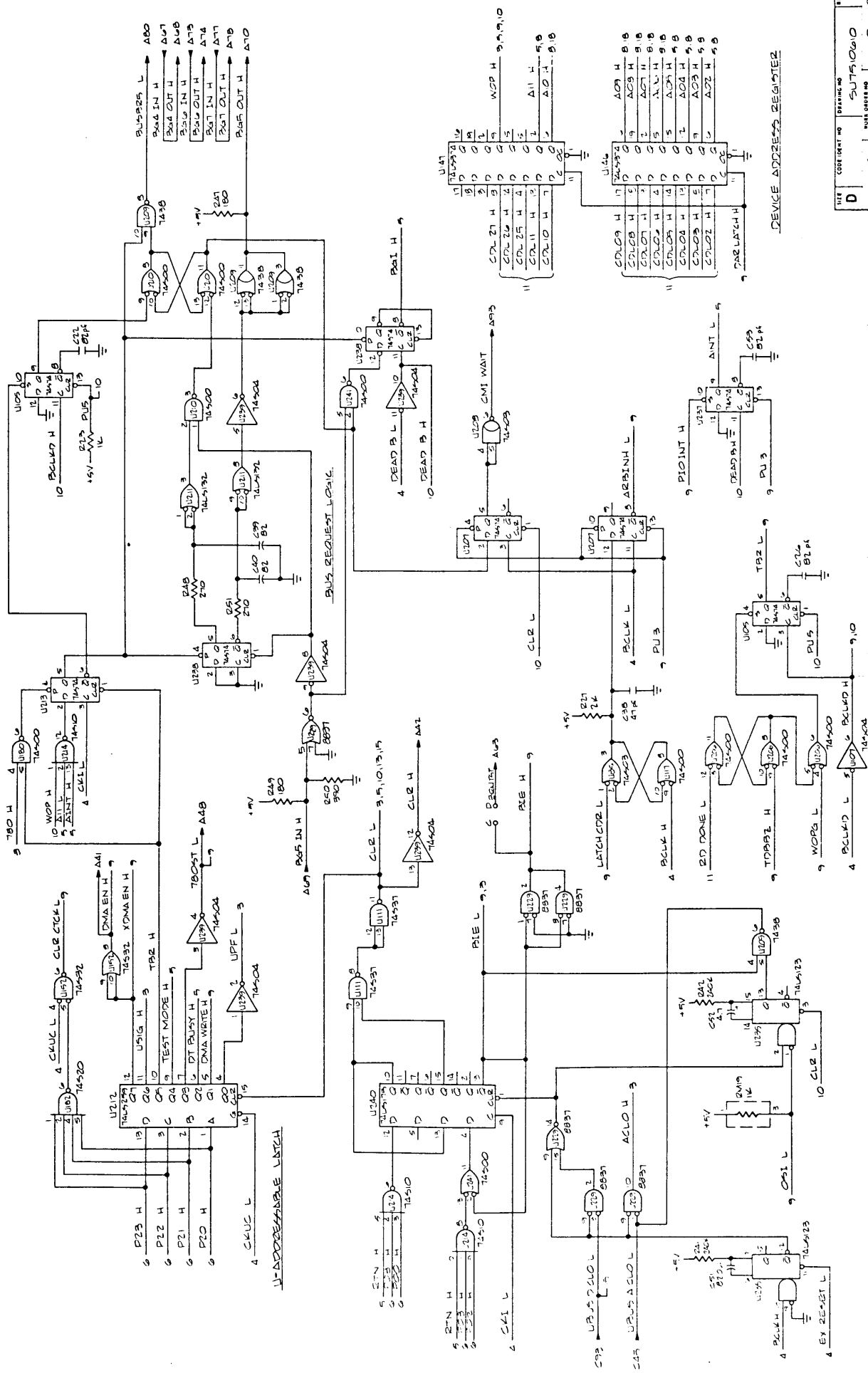
BUFFER MEMORY EXPANSION
(U42 - U45)



BUFFER ADDRESS REGISTERS

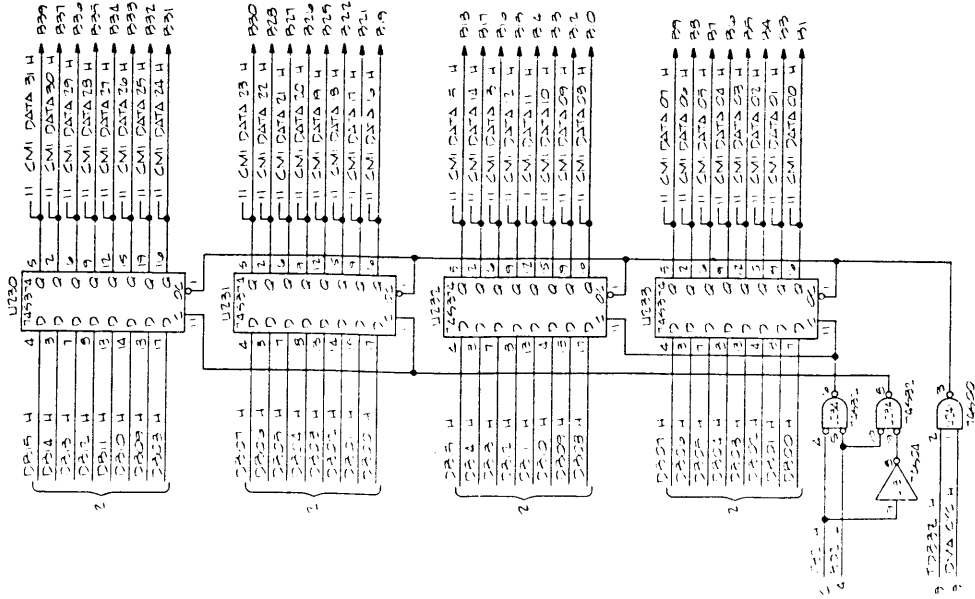


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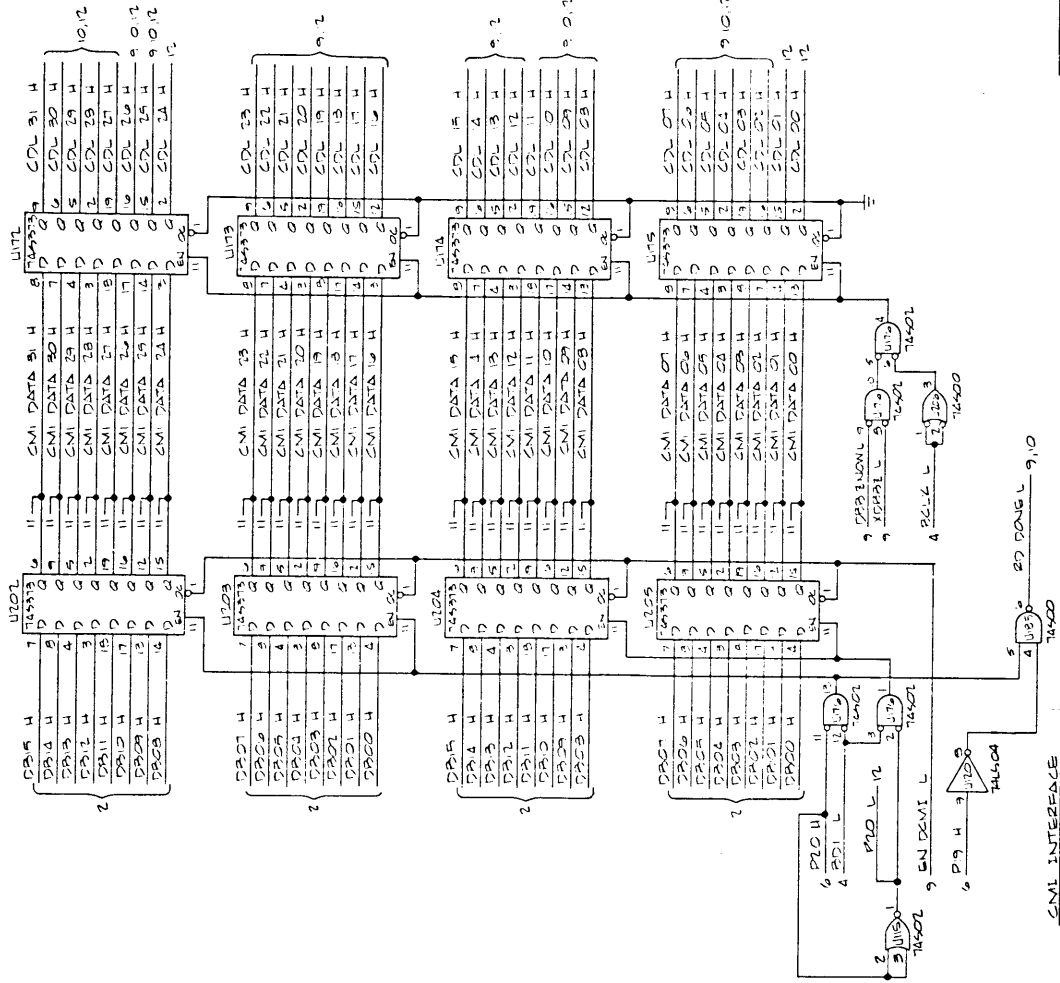


DEVICE ADDRESS REGISTER

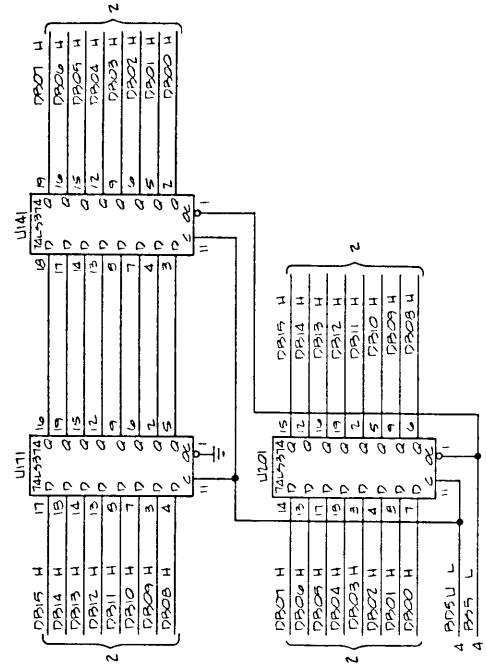
SMI ADDRESS REGISTER



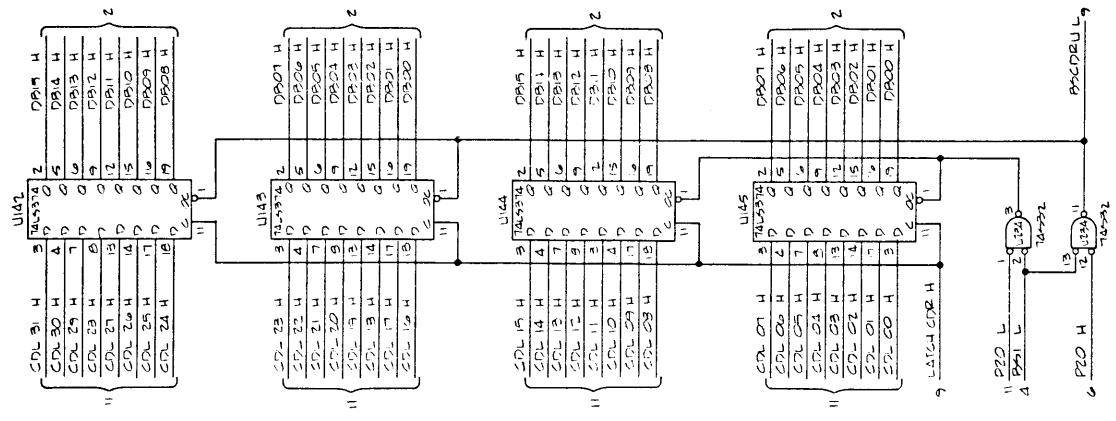
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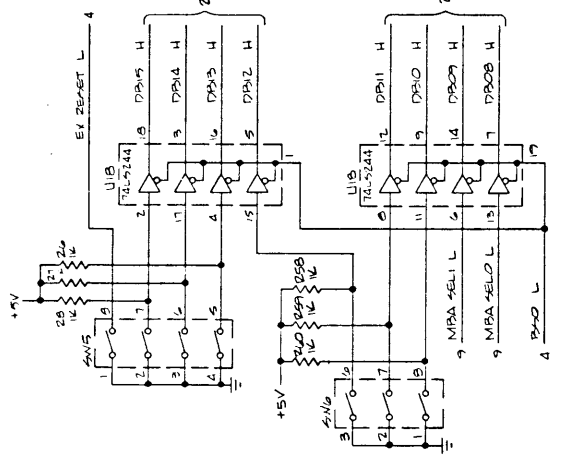
SMI INTERFERENCE



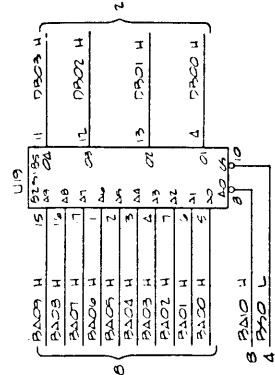
BYTE OFFSET REGISTER



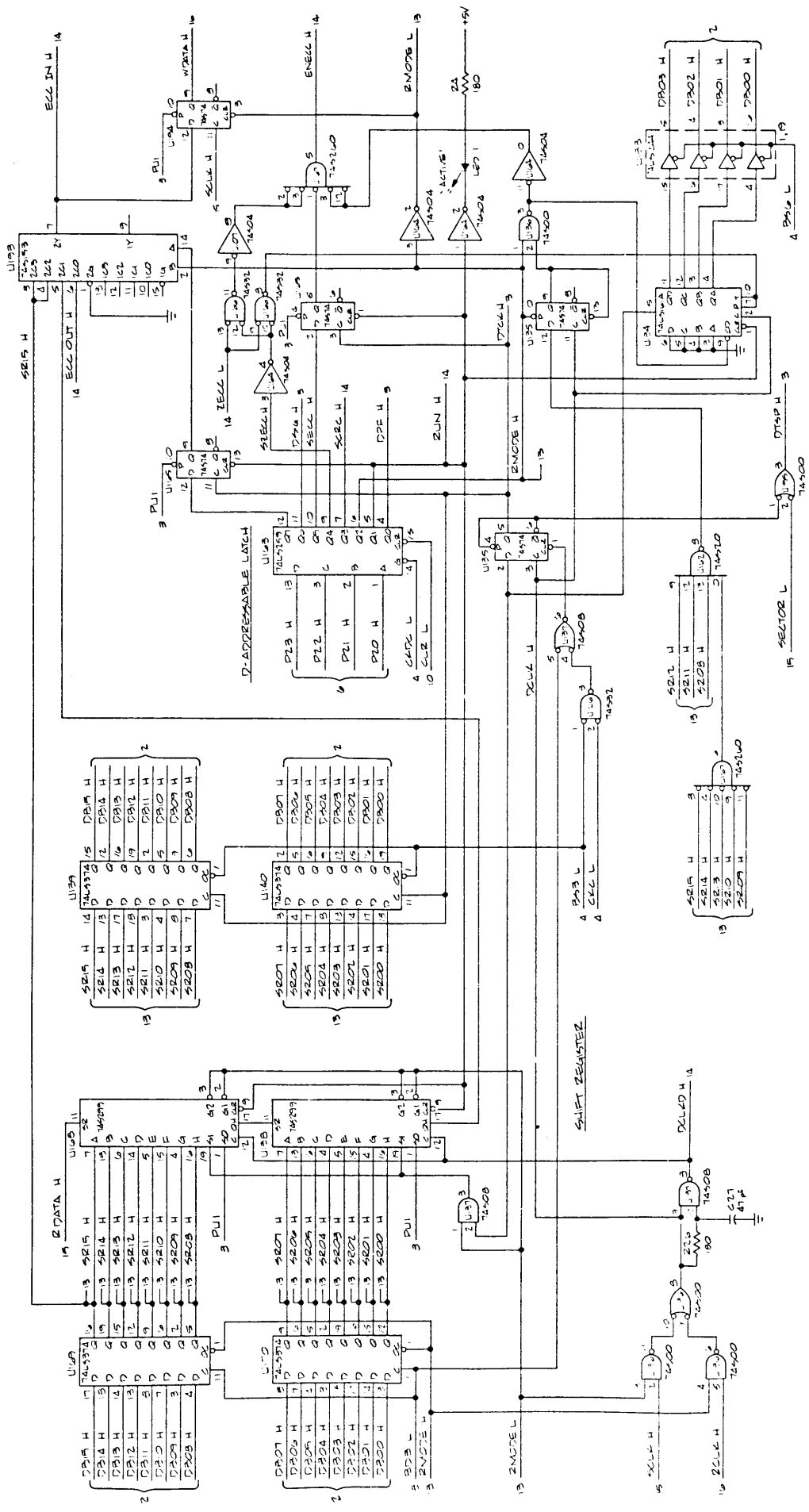
CML DATA REGISTER (INPUT)



RESET OPTION SWITCHES

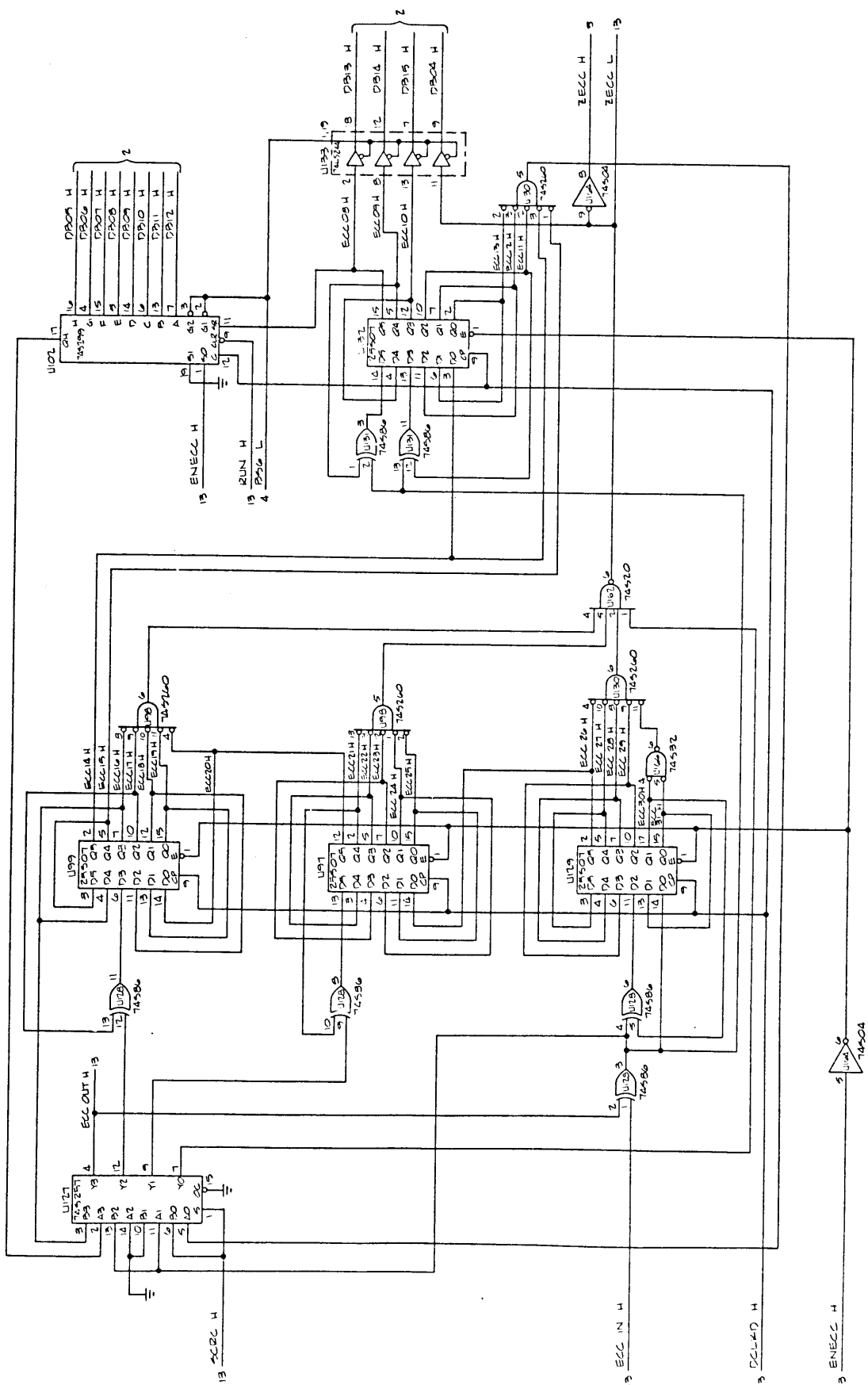


CONFIGURATION PROM

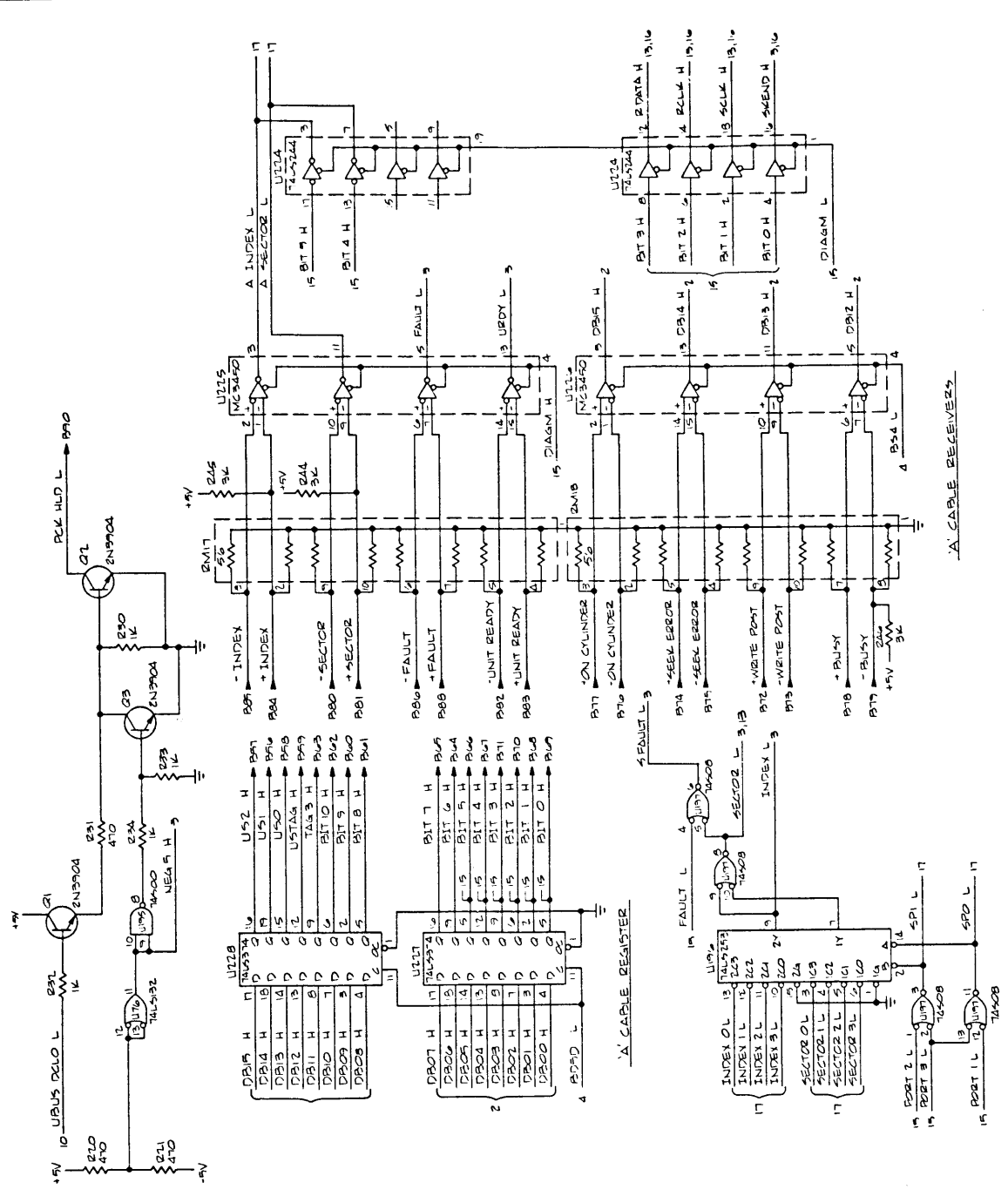


BVI/WOOD COUNTER

SYNC DETECTOR

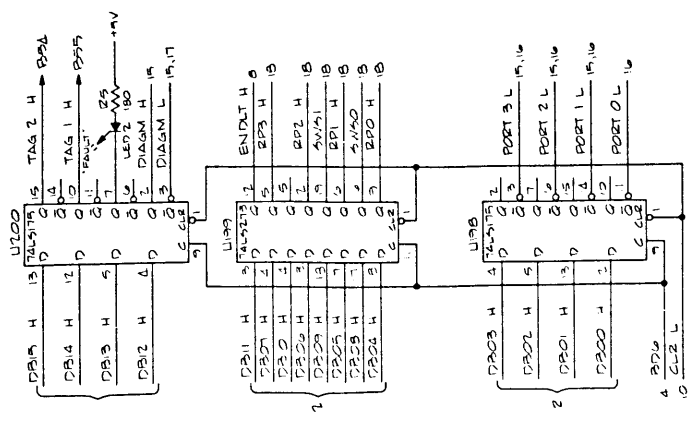


ECC REGISTER

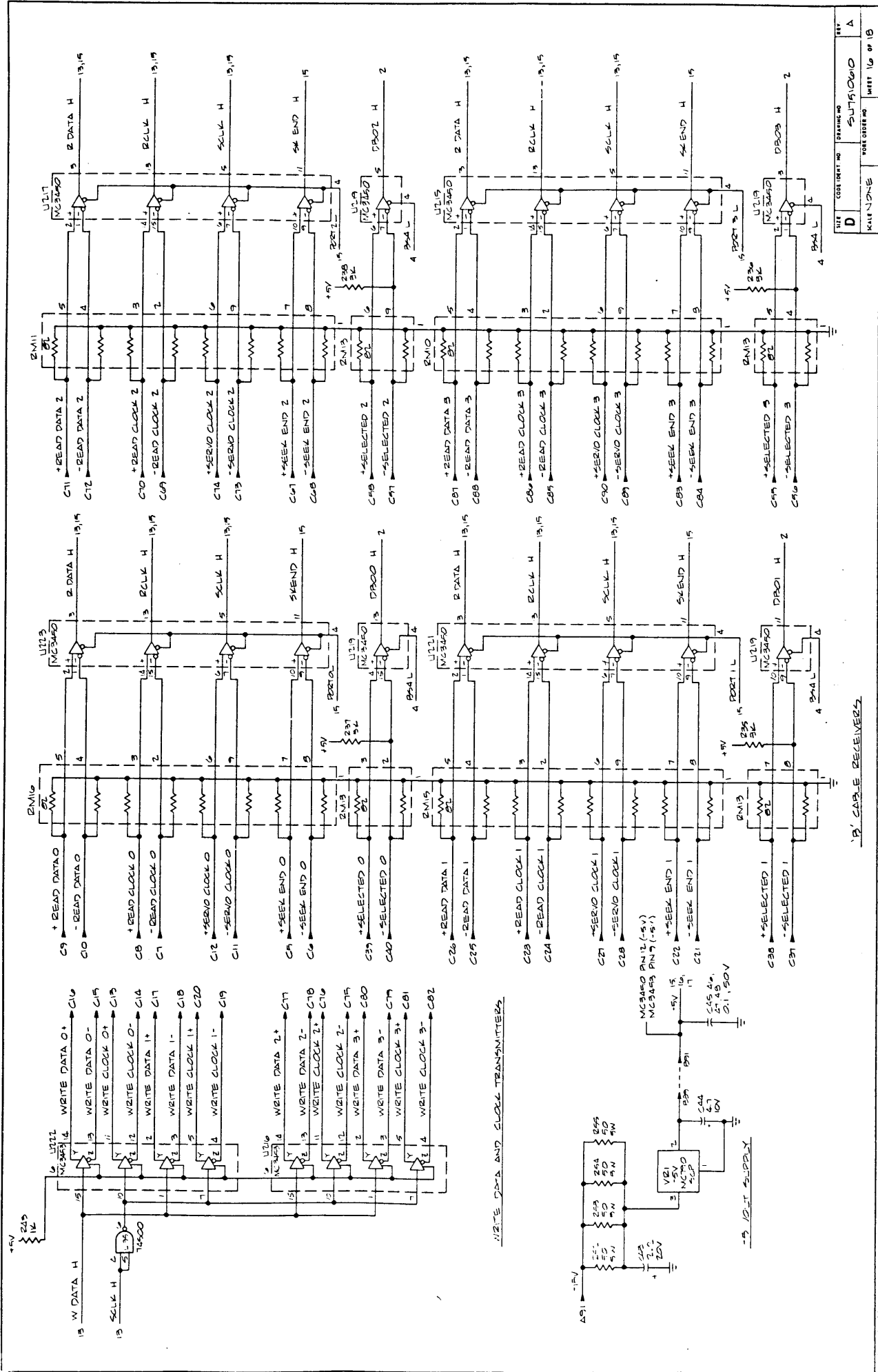


'A' CABLE RECEIVERS

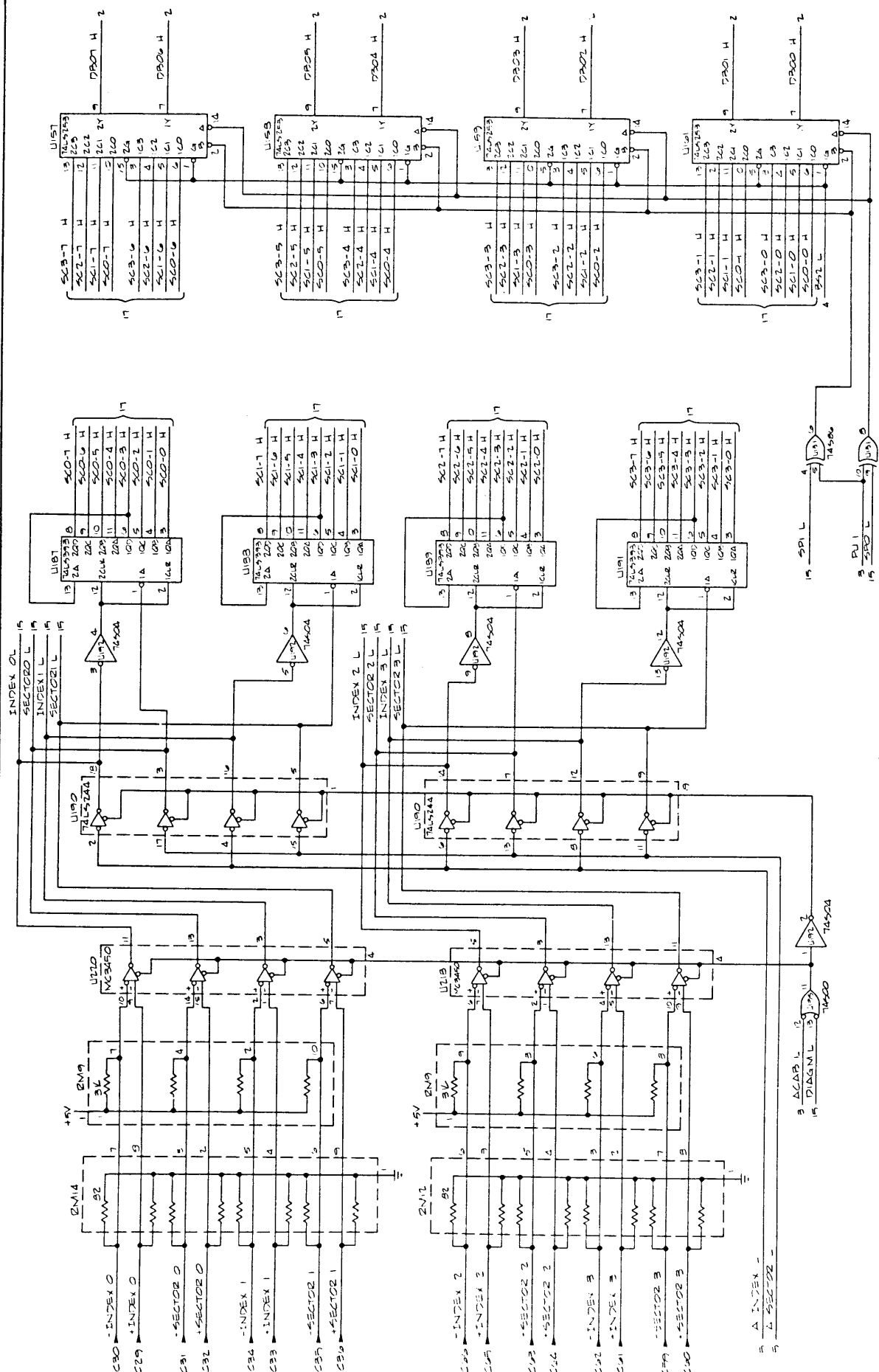
INDEX AND SECTOR MULTIPLEXER



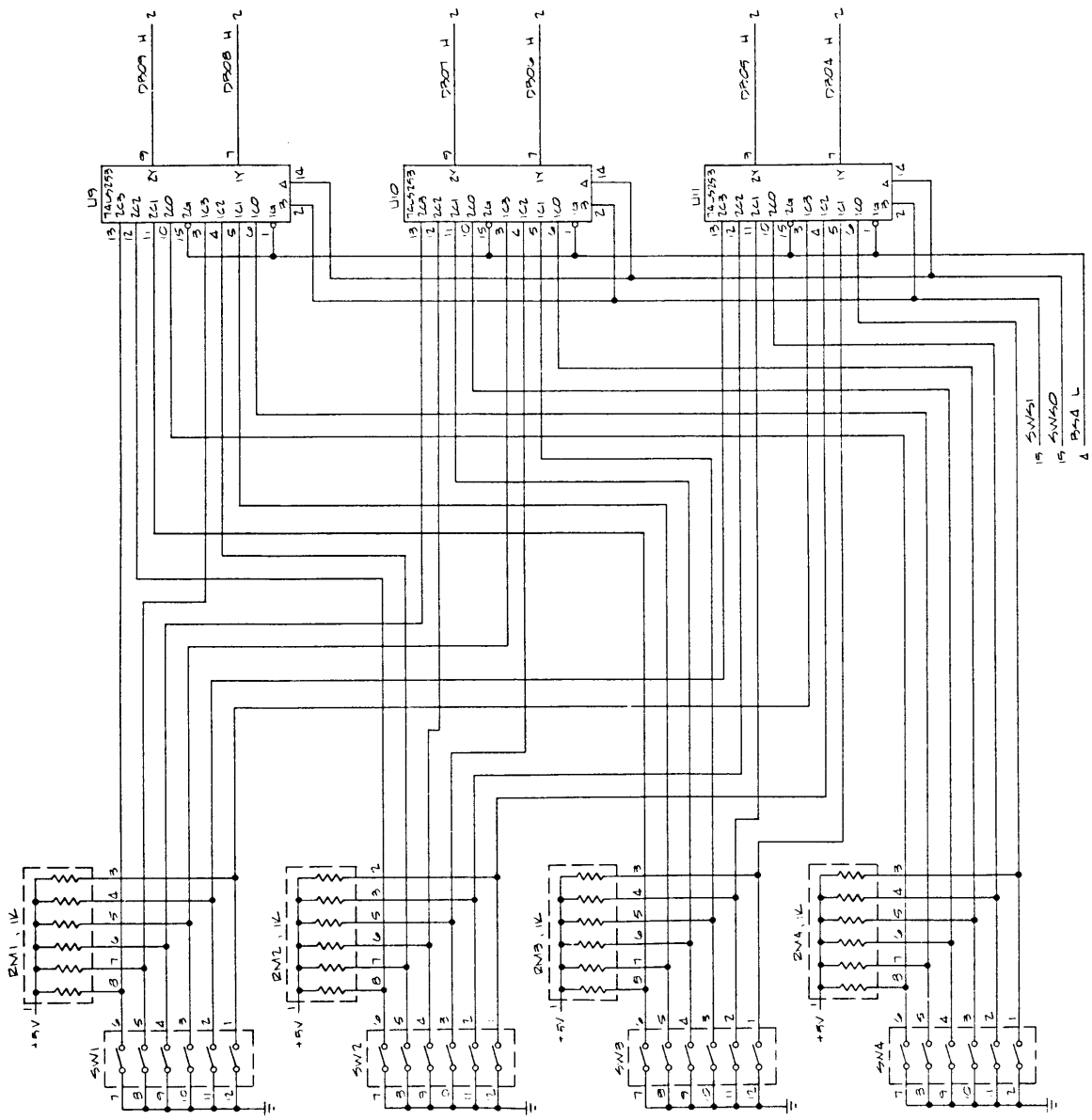
CONTROL REGISTER



12-BIT CABLE RECEIVERS

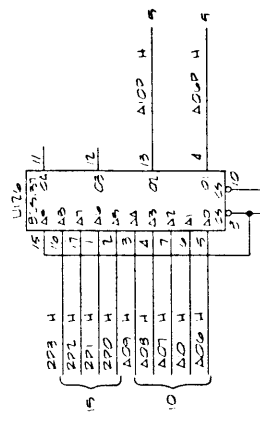


SECTOR COUNTERS



15 SWA3
15 SWA0
4 B7A L

DATA DRIVE CONFIGURATION SWITCHES



INTERRUPT VECTORS FROM