

ZONE	REV	DESCRIPTION	DATE	APPROVED
A	ELC-1159	INITIAL RELEASE	10 MAR 64	W.C.A.
B	ELC-1159	INCORPORATED	7 MAR 64	W.C.A.

TEST CONNECTORS

J1

1	OV
2	DR2 0
3	DR3 0
4	CR3 0
5	DR3 1
6	CR3 1
7	DR3 2
8	CR3 2
9	OV
10	DR2 2
11	DR3 2
12	CR3 2
13	DR3 3
14	CR3 3
15	OV
16	OV

J4

1	OV
2	OV
3	OV
4	SPARE
5	EX STOP H
6	EX STOP L
7	OV
8	EX RESET L
9	OV
10	EX RESET H
11	OV
12	SPARE
13	OV
14	PA10 H
15	PA10 H
16	PA09 H
17	PA08 H
18	PA07 H
19	PA06 H
20	PA05 H
21	PA04 H
22	PA03 H
23	PA02 H
24	PA01 H
25	OV
26	OV

J5

1	OV
2	DR10 H
3	DR10 H
4	DR10 H
5	DR10 H
6	DR10 H
7	DR10 H
8	DR10 H
9	DR10 H
10	OV
11	OV
12	OV
13	OV
14	OV
15	OV
16	OV
17	EX TEST H
18	OV
19	OV
20	SPARE

JUMPER LOCATIONS

Δ P.C. 1	4
FIG. 1A	1

SWITCH LOCATIONS

SW 1	3
SW 2	2
SW 3	2
SW 4	2
SW 5	7, 8

REFERENCE DESIGNATION NOT USED

25A-4702	245
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LAST REFERENCE DESIGNATION USED

CAPACITOR	225
CONNECTOR	J5
INTEGRATED CIRCUIT	L15B
LED	LED 3
RESISTOR	2A13
RESISTOR MODULE	2A15
SWITCH	SW 5
VOLTAGE REGULATOR	V21
DIODE	221
FUSE	F1

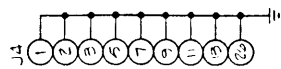
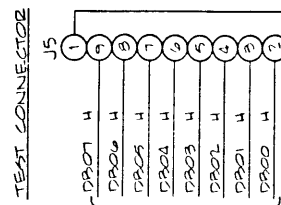
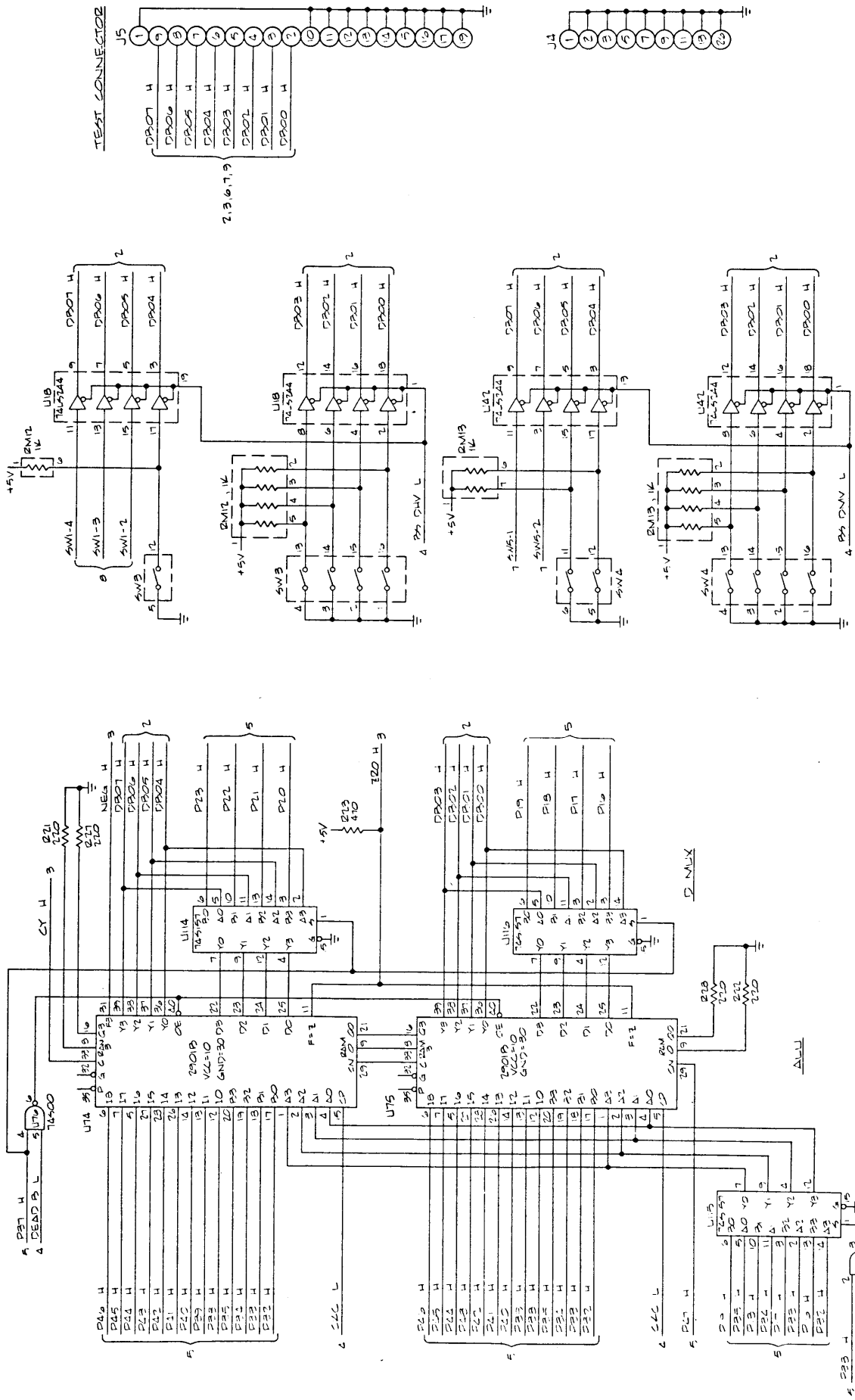
REFERENCE DESIGNATION	GATES USED	PART NUMBER
U52	5/6	74504
U53	2/4	74532
U10	2/4	74500
U11	2/6	74514
U16	3/4	74500
U101	3/4	74532
U103	2/4	74502
U104	2/4	74502
U105	4/6	74502
U106	2/4	7453
U107	1/4	74532
U108	4/4	74504
U109	4/4	74504
U110	3/4	74508
U113	2/3	74510
U127	3/4	74500
U128	5/6	74500
U132	4/4	74504
U153	3/4	74504
U154	2/6	2937
U155	5/6	2937

INTERFACE CONNECTORS

J3

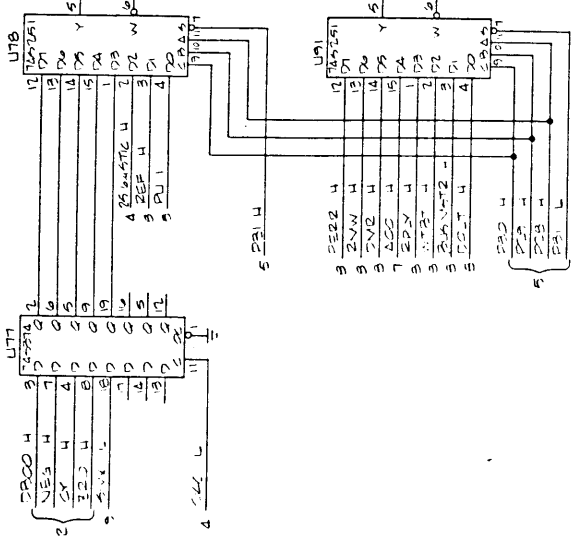
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2	CAR 8
3	DR2 9
4	CAR 9
5	DR2 10
6	CAR 10
7	DR2 11
8	CAR 11
9	DR2 12
10	CAR 12
11	DR2 13
12	CAR 13
13	DR2 14
14	CAR 14
15	DR2 15
16	CAR 15
17	OV
18	OV
19	TRANS 8
20	TRANS 9
21	TRANS 10
22	TRANS 11
23	TRANS 12
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25	TRANS 14
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4	

VECTOR INTERRUPT ADDRESS CODE

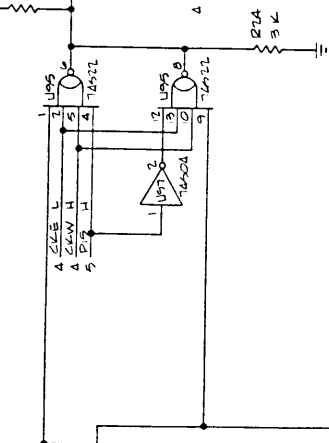


2, 3, 6, 7, 9

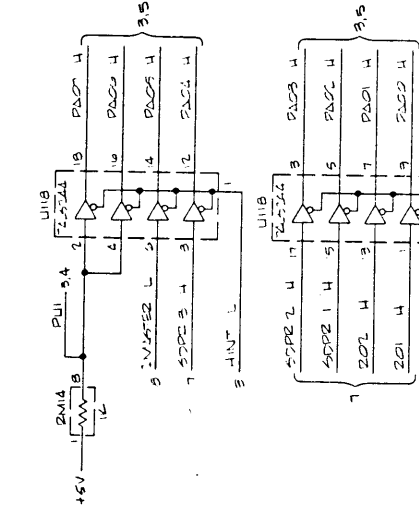
TEST CONDITIONS



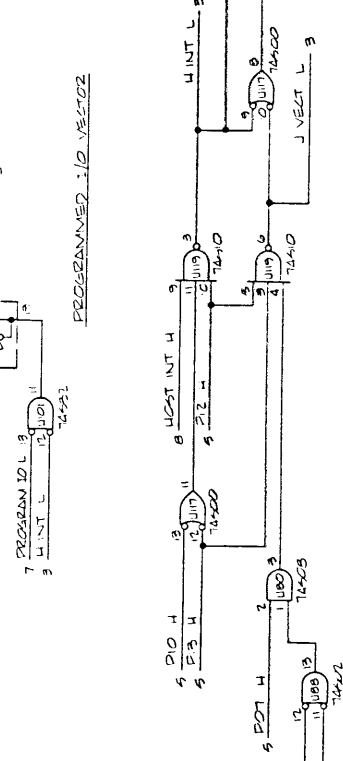
TEST SAMPLING



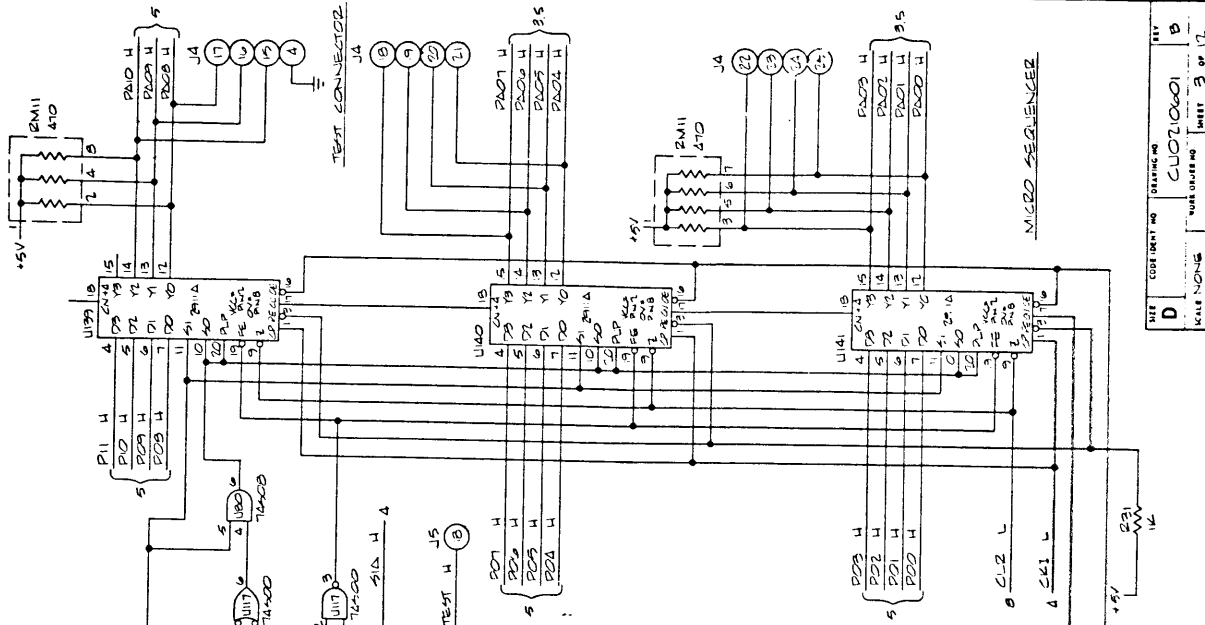
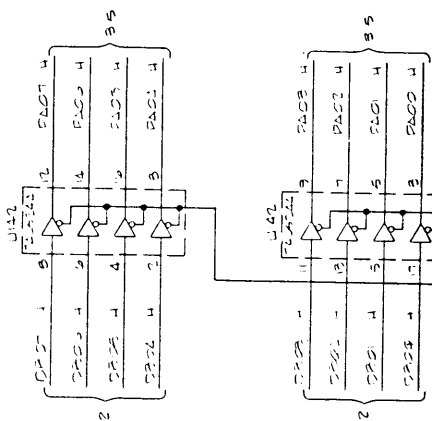
RISK AND P/O VECTOR

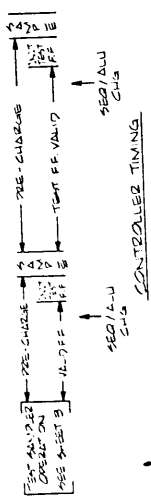
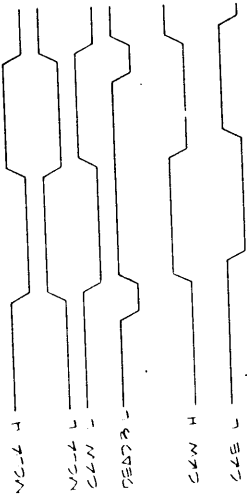
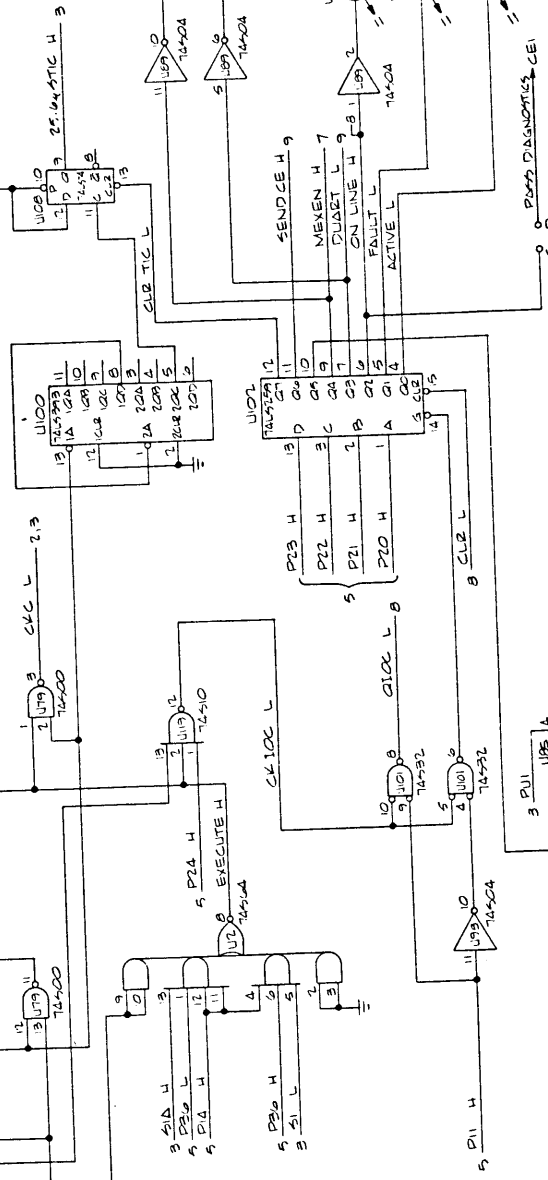
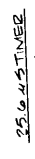
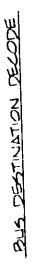
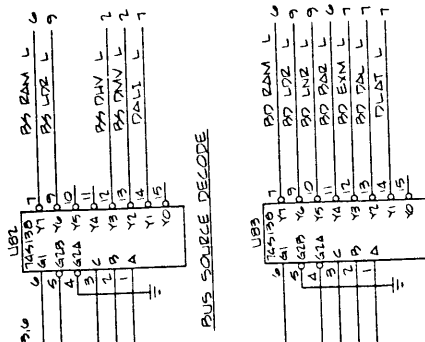


PROGRAMMED I/O VECTOR



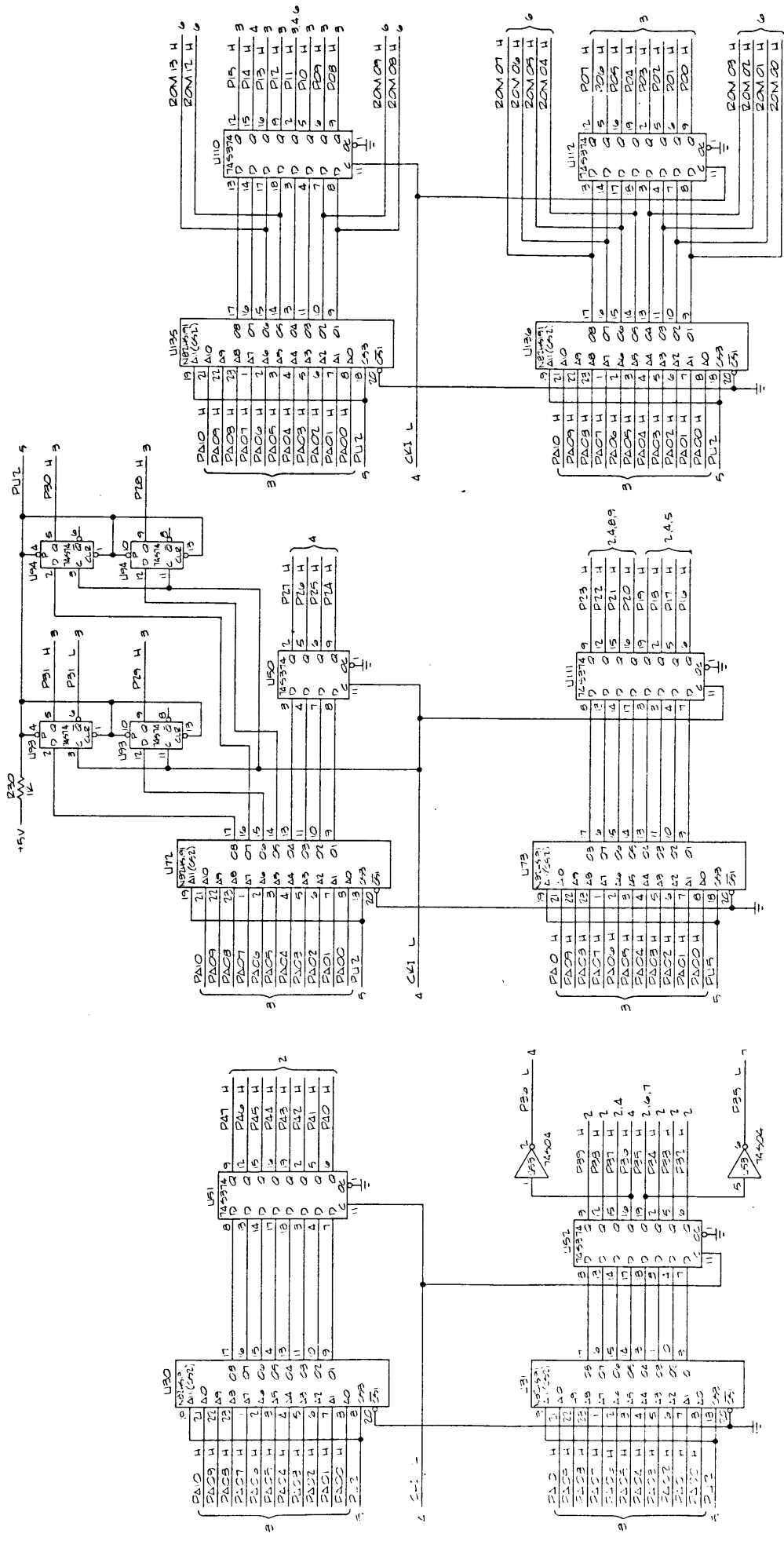
READ VECTOR

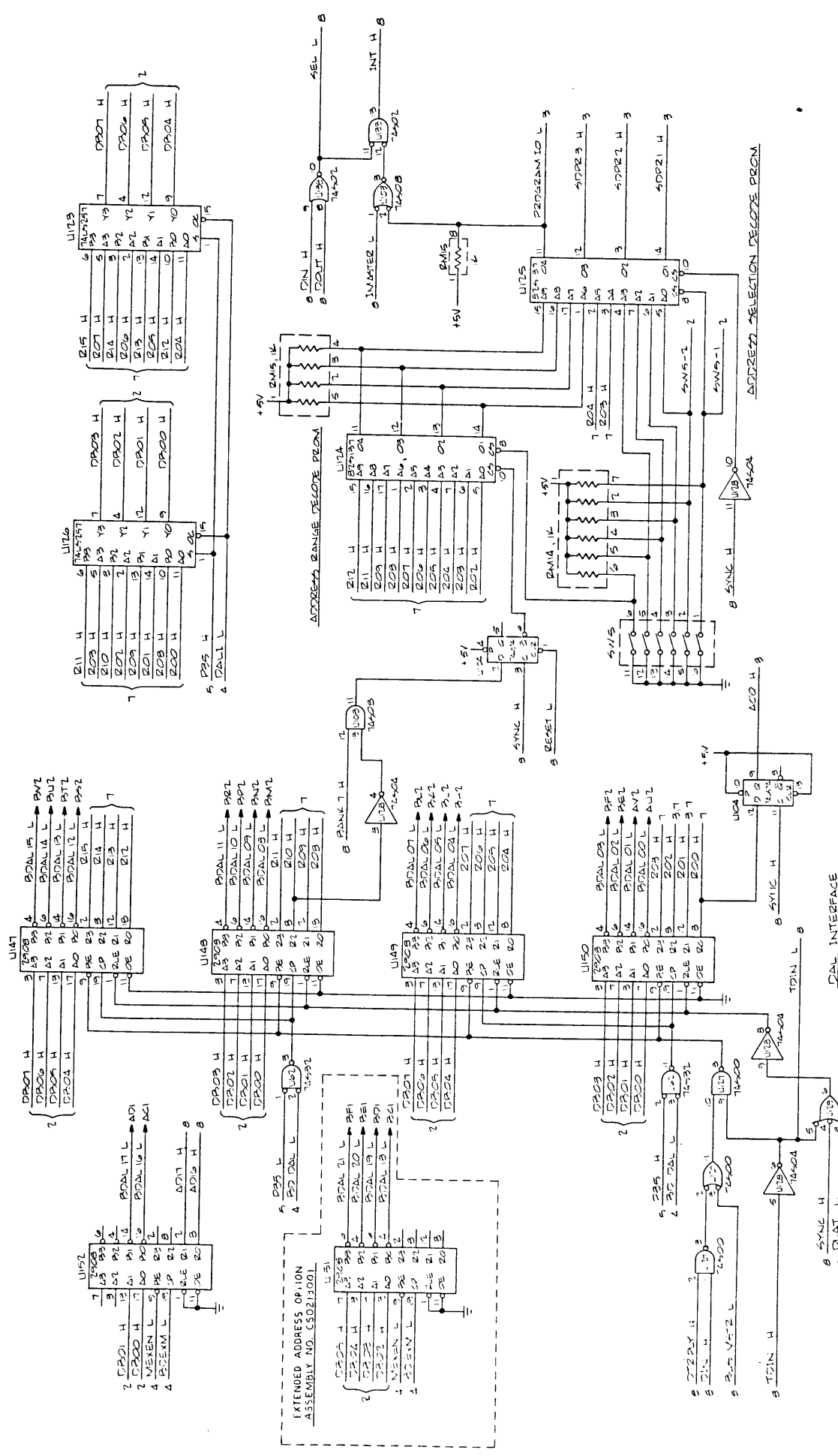




I/O CONTROL REGISTER

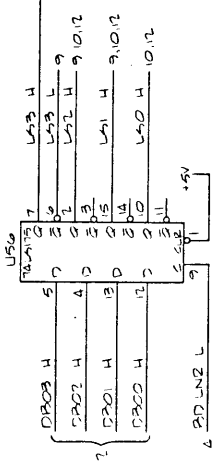
REV	DATE	DESIGNED BY	DATE
B	D	CUOZIOG01	
SCALE NONE	TORQUE CODE NO		SHEET 4 OF 12



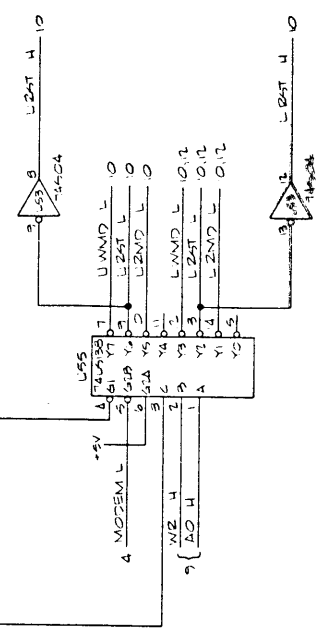
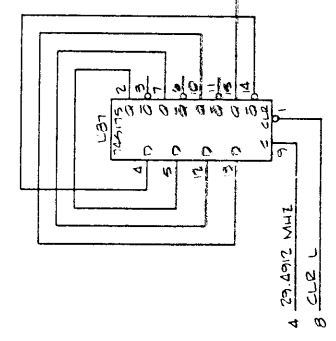
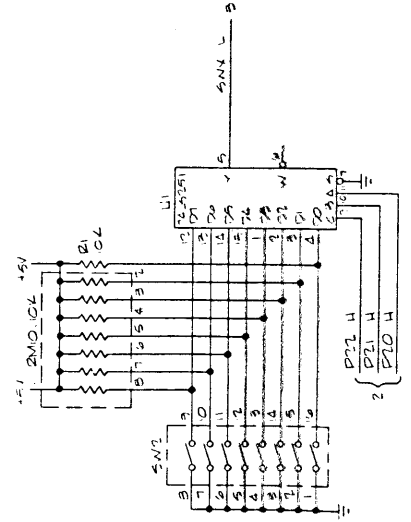
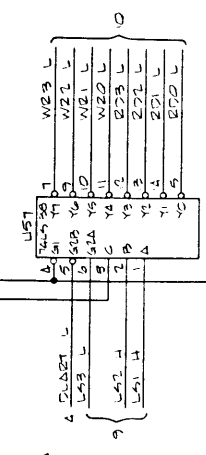
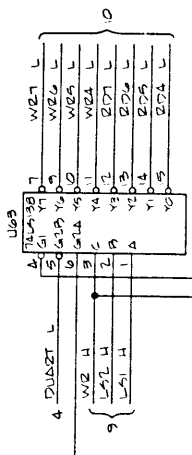
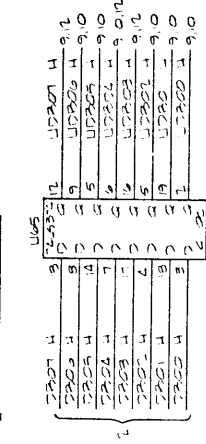
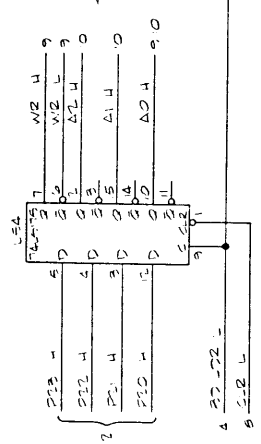
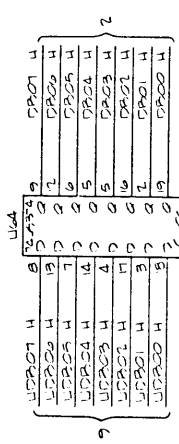


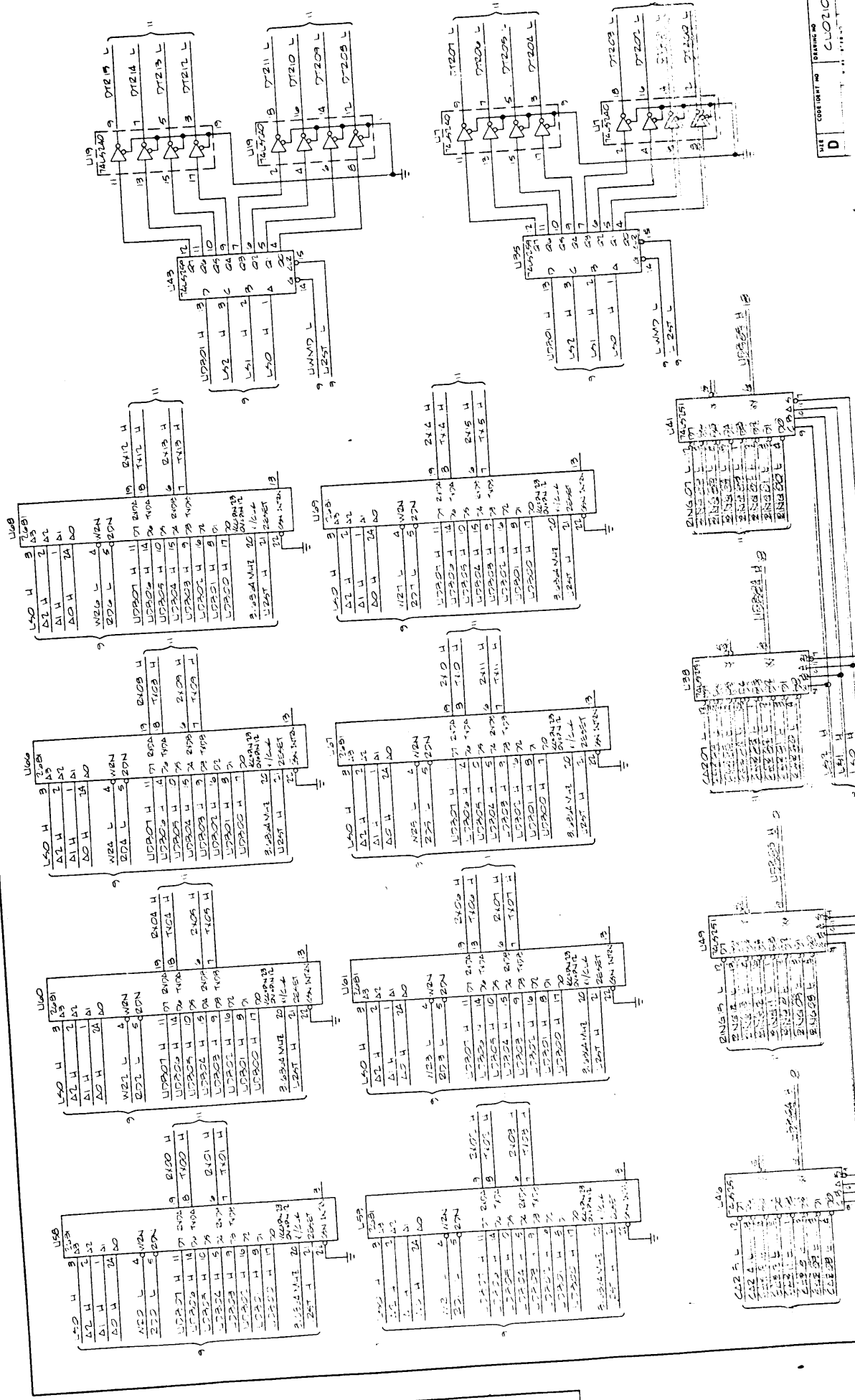
DAL INTERFACE

LINK NUMBER REGISTER



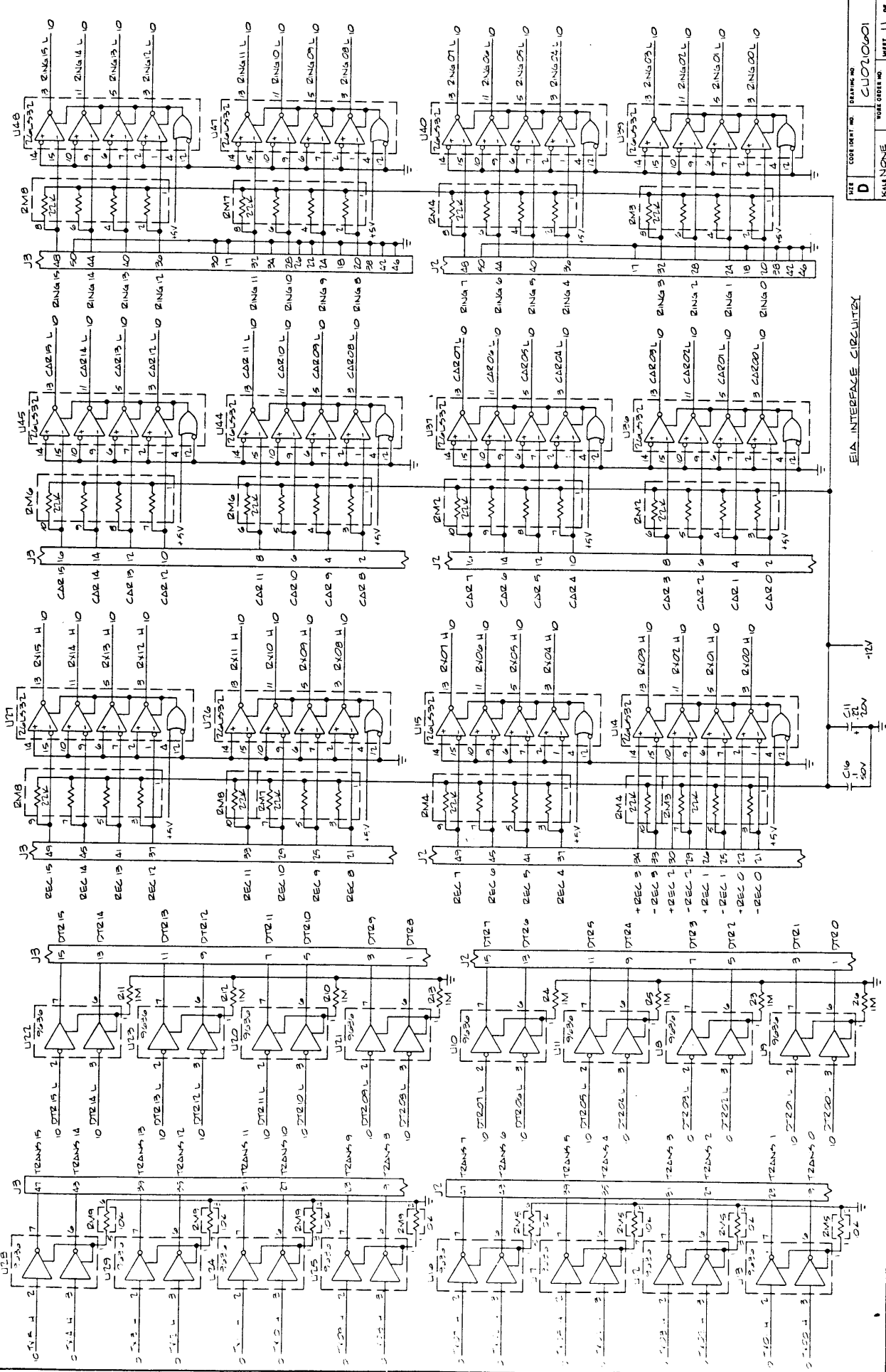
LINK DATA REGISTER



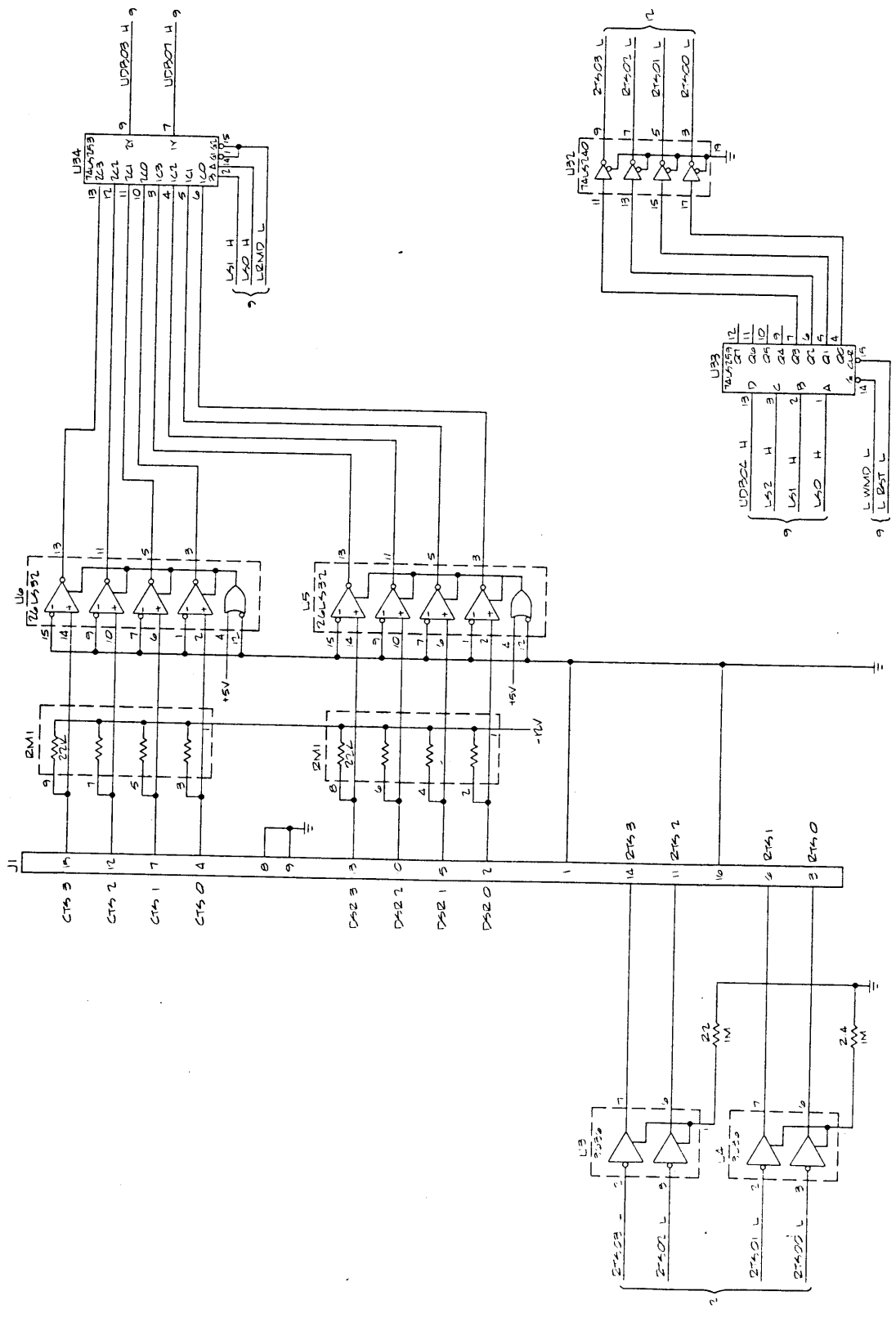


CLOCK 100 K

U08	74LS00	1	1
U09	74LS00	1	1
U10	74LS11	1	1
U11	74LS123	1	1
U12	74LS14	1	1
U13	74LS153	1	1
U14	74LS161	1	1
U15	74LS162	1	1
U16	74LS163	1	1
U17	74LS164	1	1
U18	74LS165	1	1
U19	74LS169	1	1



EIA INTERFACE CIRCUITRY



EIA INTERFACE CIRCUITRY

MODEM CONTROL SIGNAL MULTIPLEXING